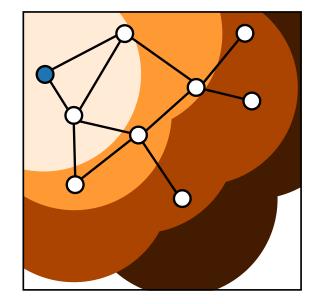
Discrete Event Systems Verification of Finite Automata (Part 1)



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ETH Zurich (D-ITET)

November 24, 2022

Most materials from Lothar Thiele and Romain Jacob

What are finite automata useful for?

What are finite automata useful for? specification

- Digital circuits
- Protocols (e.g. BGP)

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Protocols (e.g. BGP)

 Anything specified with automata

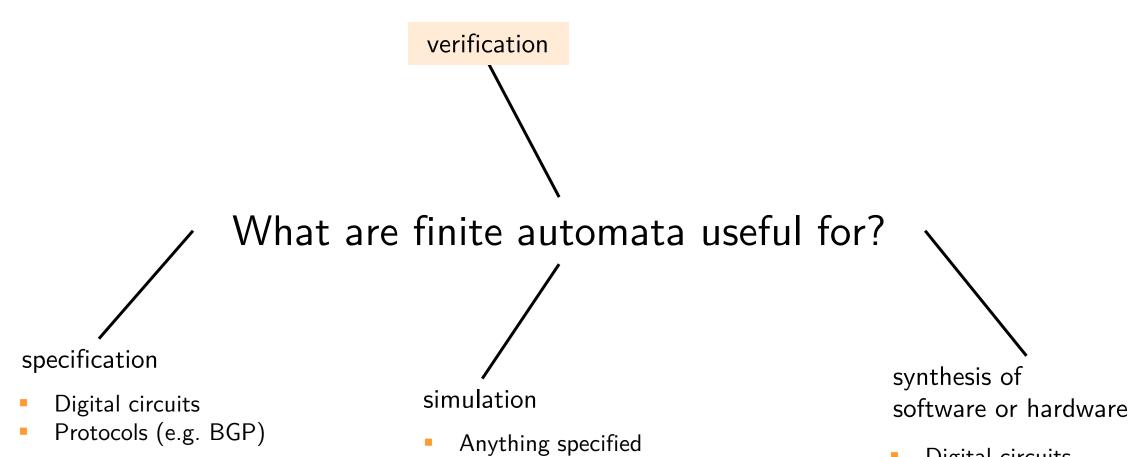
What are finite automata useful for? specification synthesis of simulation Digital circuits software or hardware

Protocols (e.g. BGP)

Anything specified with automata

Hardware components

Network configurations



with automata

Digital circuitsNetwork configurations

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- Do implementation and specification describe the same behavior?
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 - Unless the simulation is exhaustive, i.e., all possible input sequences are tested, the result is not trustworthy.
 - In general, simulation can only show the presence of errors but not the absence (correctness).

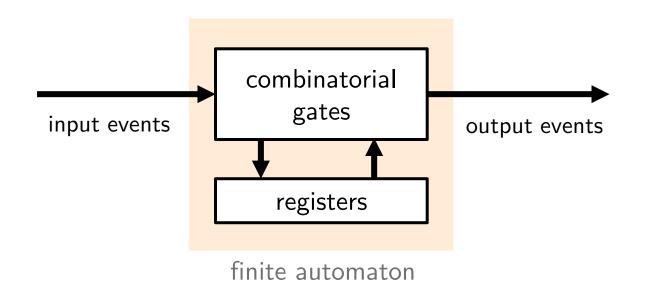
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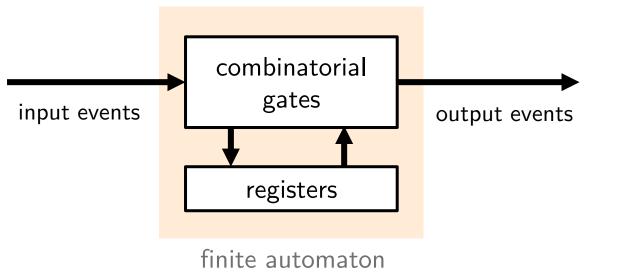
Possible solutions:

- Simulation (sometimes also called validation or testing)
 - Unless the simulation is exhaustive, i.e., all possible input sequences are tested, the result is not trustworthy.
 - In general, simulation can only show the presence of errors but not the absence (correctness).
- Formal analysis (sometimes also called verification)
 - Formal (unambiguous) proof of correctness.

- Due to the finite number of states, proving properties of a finite state machine can be done by enumeration.
- As computer systems have finite memory, properties of processors (and embedded systems in general) could be shown in principle.



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- As computer systems have finite memory, properties of processors (and embedded systems in general) could be shown in principle.
- But is enumeration a reasonable approach in practice?



memory	number of states	
8 Bit	256	
32 Bit	4.10 ⁹	
1KBit	10 ³⁰⁰	
1MBit	10300 000	12
1GBit	10300 000 000	12

atoms in the universe is about $10^{\rm 82}$

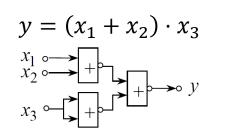
- There have been **major breakthroughs** in recent years on the verification of finite automata with very large state spaces. Prominent methods are based on
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 - transformation to a Boolean Satisfiability (SAT) problem (not covered in this course) and
 - symbolic model checking via binary decision diagrams (covered in this course).
- **Symbolic model checking** is a method of verifying temporal properties of finite (and sometimes infinite) state systems that relies on a symbolic representation of sets, typically as Binary Decision Diagrams (BDD's).
- Verification is used in industry for proving the correctness of complex digital circuits (control, arithmetic units, cache coherence), safety-critical software and embedded systems (traffic control, train systems, security protocols).

Verification Scenarios

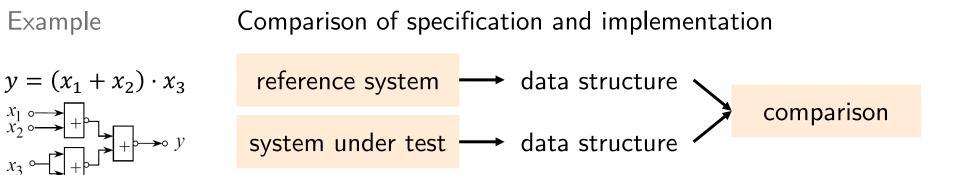
Example

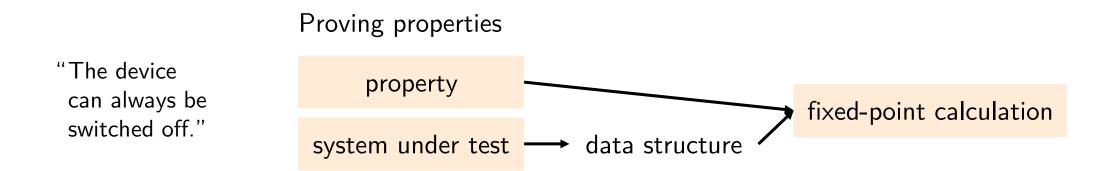
Comparison of specification and implementation



Verification Scenarios

Example





Efficient state representation

Set of states as Boolean function

Binary Decision Diagram representation

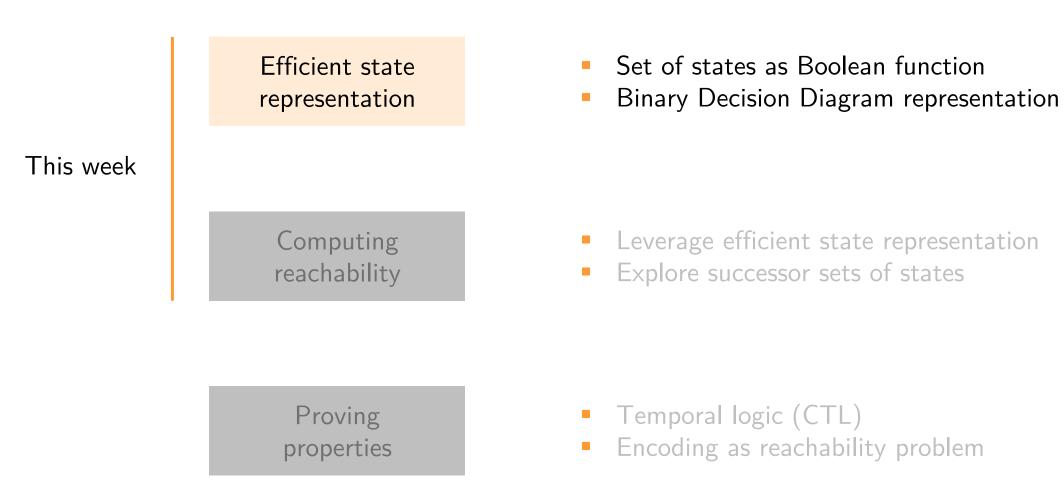
Computing reachability

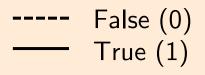
Leverage efficient state representation

Explore successor sets of states

Proving properties

- Temporal logic (CTL)
- Encoding as reachability problem

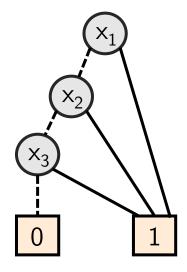




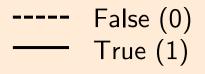
• Concept

- Data structure that allows to represent Boolean functions.
- The representation is unique for a given ordering of variables. If the ordering of variables is fixed, we call it an ordered BDD (OBDD).

$$f = x_1 + x_2 + x_3$$



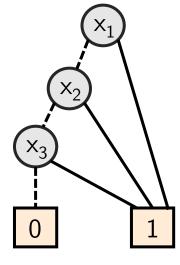
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 - Edges are labeled with input values.
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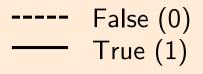
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 $f = x_1 + x_2 + x_3$ f(1,0,1) = ?



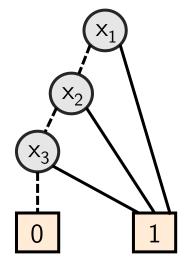
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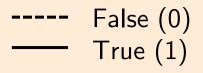
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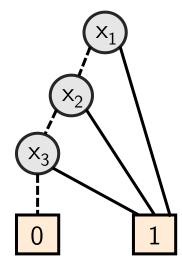
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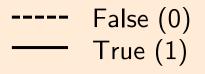
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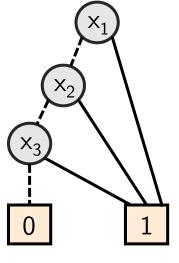
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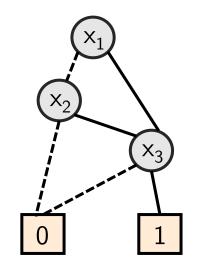
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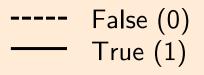
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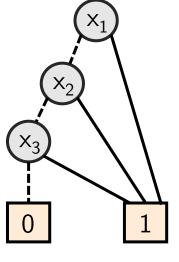




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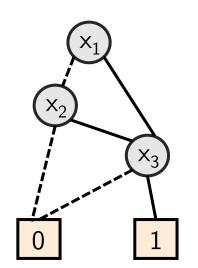
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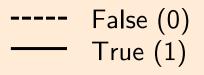


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 $g(0,1,0) = ?$

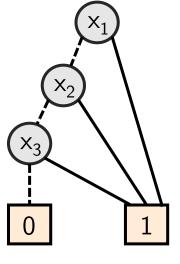




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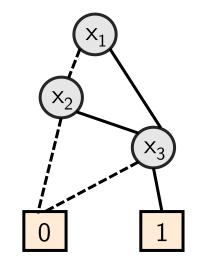
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$$g = (x_1 + x_2) \cdot x_3$$

$$g(0,1,0) = false$$



Basic concept of verification using BDDs

- BDDs represent Boolean functions.
- Therefore, they can be used to describe sets of states and transformation relations.
- Due to the unique representation of Boolean functions, *reduced ordered* BDDs (ROBDD) can be used to proof equivalence between Boolean functions or between sets of states.
- BDDs can easily and efficiently be manipulated.

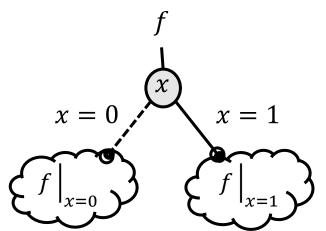
Logic	Boolean	Binary
OR	+	V
AND	•	Λ
NOT	$\overline{\mathbf{X}}$	\neg or \overline{X}

BDDs are based on the Boole-Shannon-Decomposition:

$$f = \bar{x} \cdot f \Big|_{x=0} + x \cdot f \Big|_{x=1}$$

A Boolean function has two co-factors for each variable, one for each evaluation

- $f|_{x=0}$: remaining function for x = 0
- $f|_{x=1}$: remaining function for x = 1



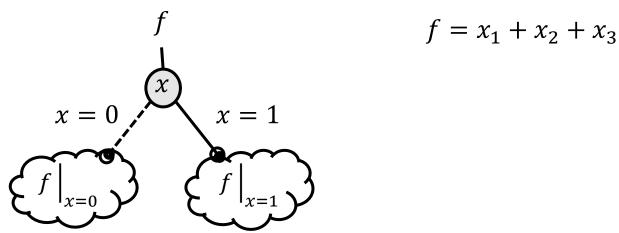
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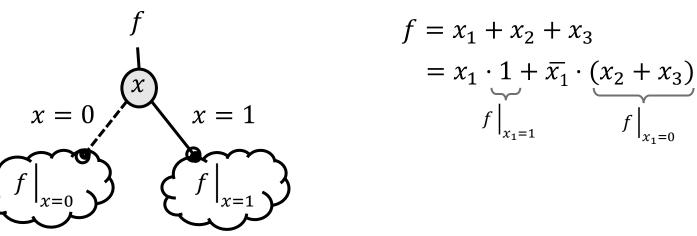
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 (x_1)

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$$f \qquad f =$$

$$x = 0 \qquad x = 1$$

$$f =$$

$$x = 1$$

$$f =$$

$$= x_{1} + x_{2} + x_{3}$$

= $x_{1} \cdot 1 + \overline{x_{1}} \cdot (x_{2} + x_{3})$
 $x_{2} + \overline{x_{2}} \cdot x_{3}$

$$(x_1)$$

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$$f$$

$$x = 0$$

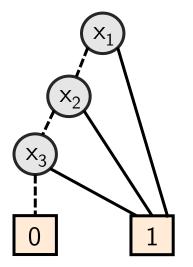
$$x = 1$$

$$f|_{x=0}$$

$$f|_{x=1}$$

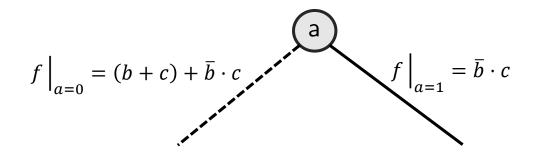
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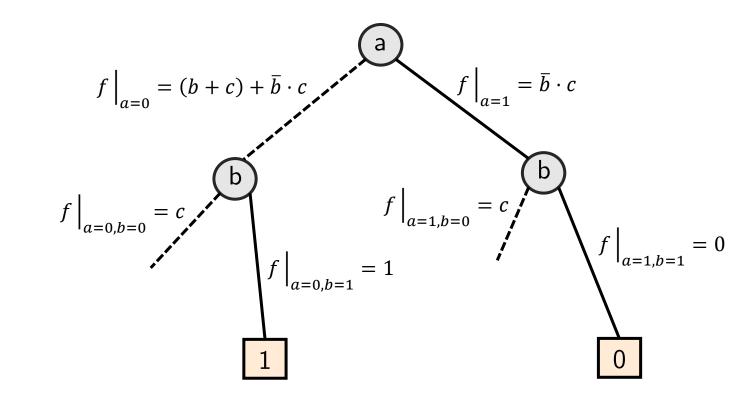


 $f(a,b,c) = \overline{a} \cdot (b+c) + \overline{b} \cdot c$

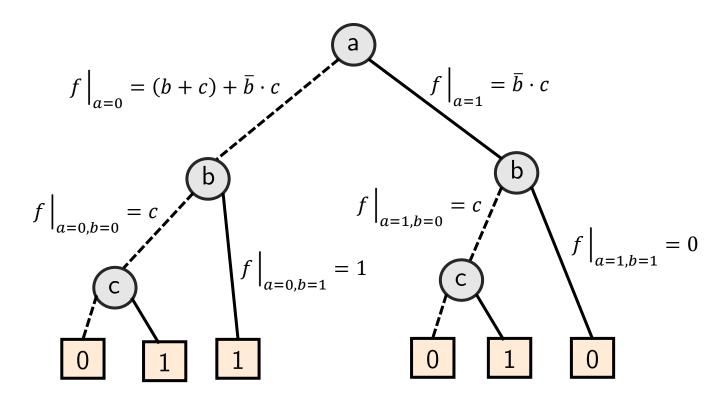
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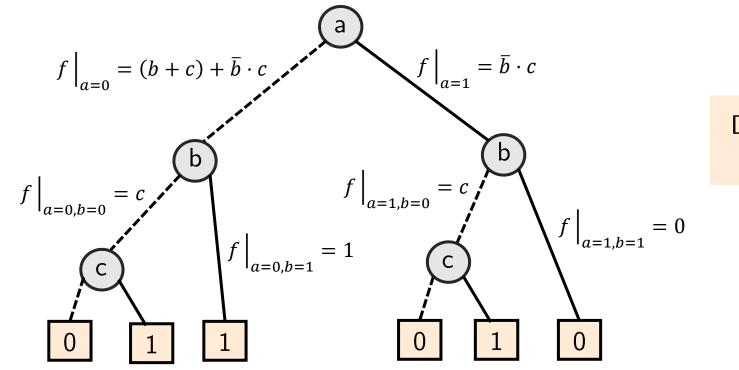


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Ordering: $a \rightarrow b \rightarrow c$



Does variable order matter?

Variable Order

- If we fix the ordering of variables, BDDs are called OBBDs (Ordered Binary Decision Diagrams).
- The ordering is essential for the size of a BDD.

$$f = (a \cdot b) + (c \cdot d) + e$$

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$$a \rightarrow b \rightarrow c \rightarrow d \rightarrow e$$

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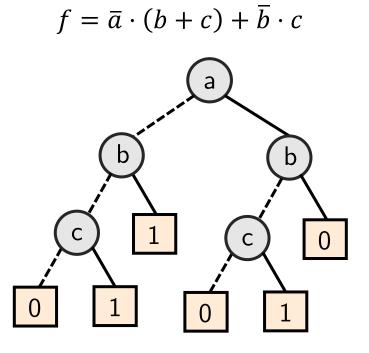
F

d

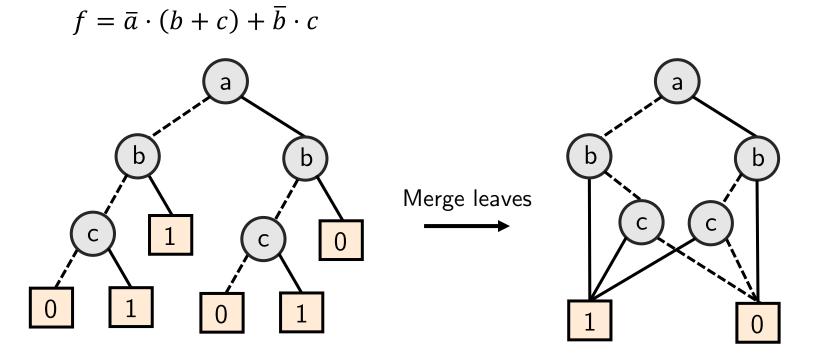
(a)

- **SIMPLIFY**: Given BDD for f, determine simplified BDD for f.
 - Eliminate redundant nodes.
 - Merge equivalent leaves (0 and 1)
 - Merge isomorphic nodes, i.e., nodes that represent the same Boolean function.
 - A BDD that can not be further simplified is called a reduced BDD.
 A reduced OBDD (also denoted as ROBDD) is a unique representation of a given Boolean function.

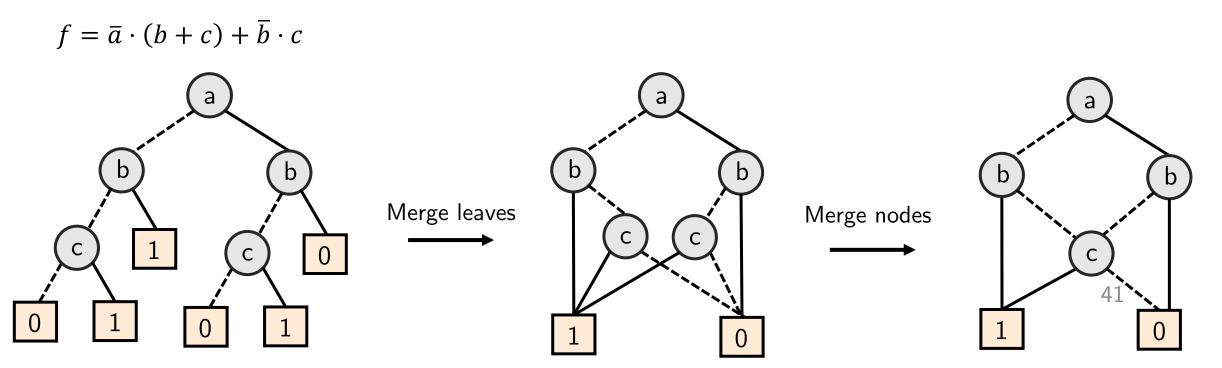
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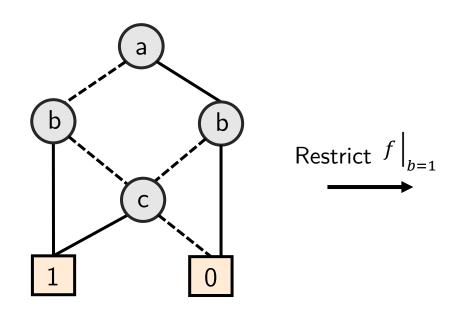
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- **RESTRICT**: Given BDD for f, determine BDD for $f|_{x=k}$.
 - Delete all edges that represent $x = \overline{k}$;
 - For every pair of edges (a x, x b) include a new edge (a b) and remove the old ones;
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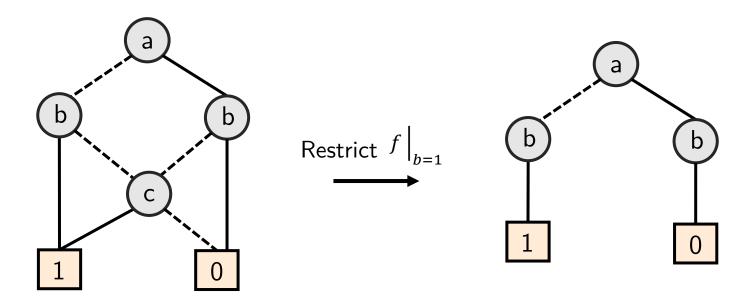
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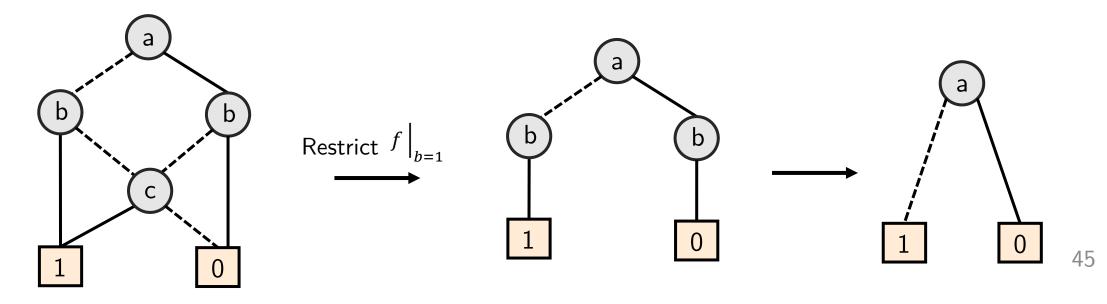
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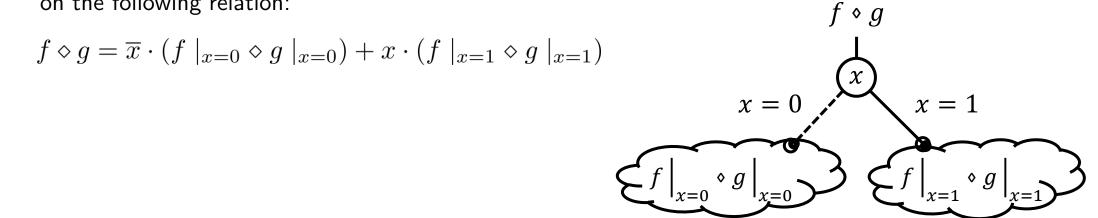


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- **APPLY**: Given BDDs for f and g, determine a BDD for $f \diamond g$ for some operation \diamond .
 - Combine the two BDDs recursively based on the following relation:



• Boolean functions can be converted to BDDs step by step using **APPLY**.

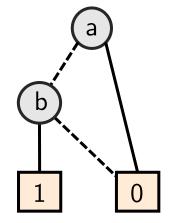
• Quantifiers are constructed by **APPLY** and **RESTRICT**:

$$(\exists x:f) \quad \Leftrightarrow \quad (f \mid_{x=0} + f \mid_{x=1})$$
$$(\forall x:f) \quad \Leftrightarrow \quad (f \mid_{x=0} \cdot f \mid_{x=1})$$
$$(\exists x_1, x_2:f) \quad \Leftrightarrow \quad (\exists x_1 \ (\exists x_2:f))$$
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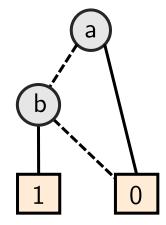
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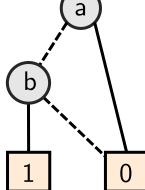
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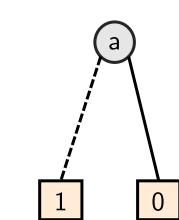


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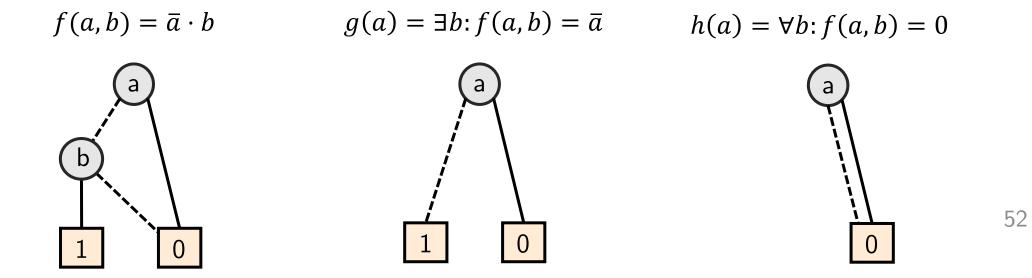
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$$(\forall x:f) \quad \Leftrightarrow \quad (f \mid_{x=0} \cdot f \mid_{x=1})$$

$$f(a,b) = \overline{a} \cdot b \qquad g(a) = \exists b : f(a,b) = \overline{a} \qquad h(a) = \forall b : f(a,b)$$

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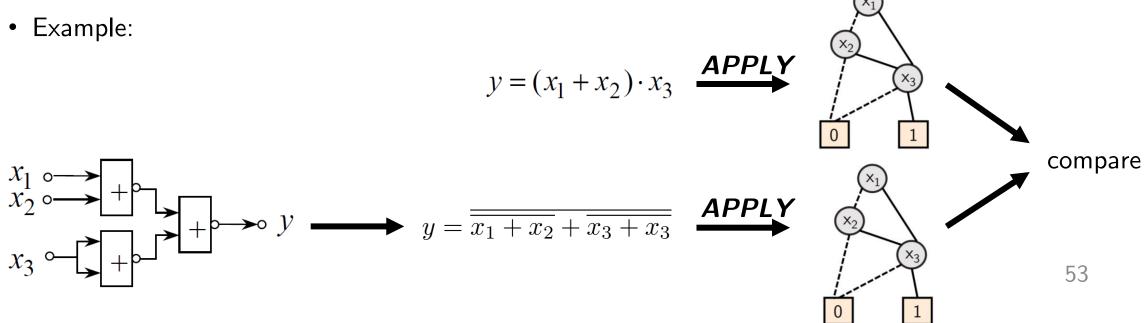
• Quantifiers are constructed by **APPLY** and **RESTRICT**:

$$(\exists x:f) \quad \Leftrightarrow \quad (f \mid_{x=0} + f \mid_{x=1})$$
$$(\forall x:f) \quad \Leftrightarrow \quad (f \mid_{x=0} \cdot f \mid_{x=1})$$

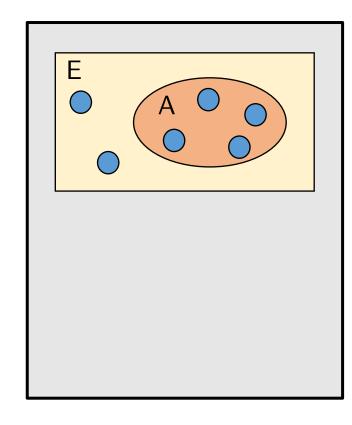


Comparison using BDDs

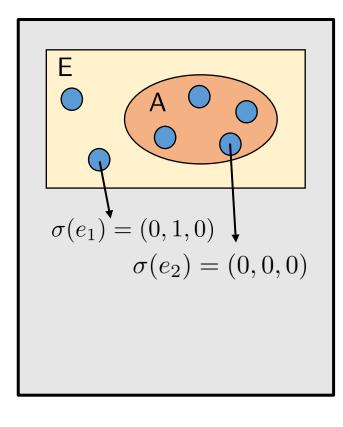
- Boolean (combinatorial) circuits: Compare specification and implementation, or compare two implementations.
- Method:
 - Representation of the two systems in ROBDDs, e.g., by applying the **APPLY** operator repeatedly.
 - Compare the structures of the ROBDDs.

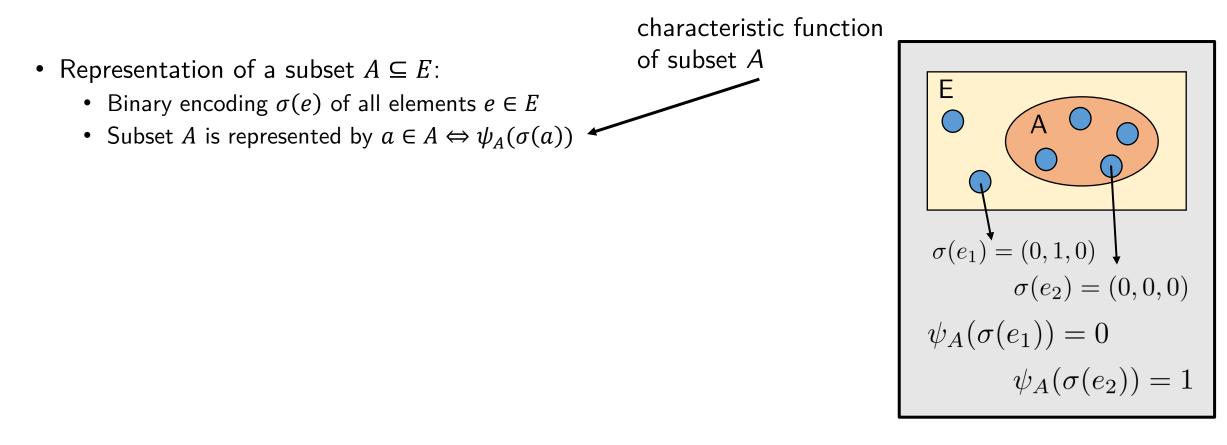


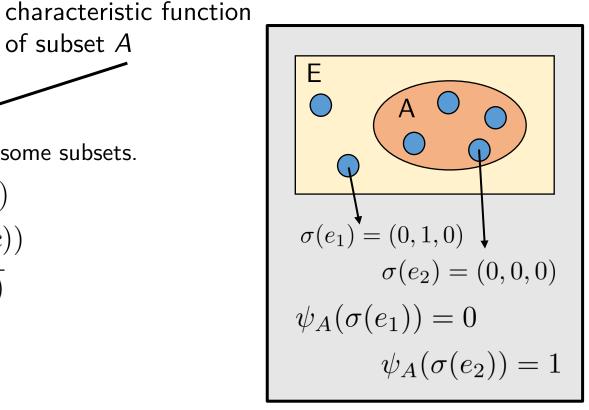
• Representation of a subset $A \subseteq E$:



- Representation of a subset $A \subseteq E$:
 - Binary encoding $\sigma(e)$ of all elements $e \in E$







- Representation of a subset $A \subseteq E$:
 - Binary encoding $\sigma(e)$ of all elements $e \in E$
 - Subset A is represented by $a \in A \Leftrightarrow \psi_A(\sigma(a))$
 - Stepwise construction of the BDD corresponding to some subsets.

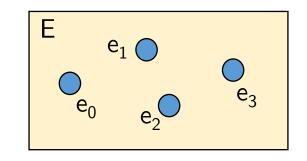
of subset A

 $c \in A \cap B \quad \Leftrightarrow \quad \psi_A(\sigma(c)) \cdot \psi_B(\sigma(c))$ $c \in A \cup B \quad \Leftrightarrow \quad \psi_A(\sigma(c)) + \psi_B(\sigma(c))$ $c \in A \setminus B \quad \Leftrightarrow \quad \psi_A(\sigma(c)) \cdot \psi_B(\sigma(c))$ $c \in E \setminus A \quad \Leftrightarrow \quad \overline{\psi_A(\sigma(c))}$

• Example:

$$\forall e \in E : \ \sigma(e) = (x_1, x_0) \\ \sigma(e_0) = (0, 0) \quad \sigma(e_1) = (0, 1) \quad \sigma(e_2) = (1, 0) \quad \sigma(e_3) = (1, 1) \\ \psi_A = x_0 \oplus x_1$$

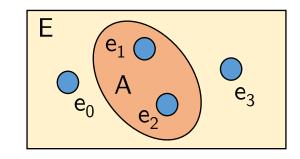
A = **?**



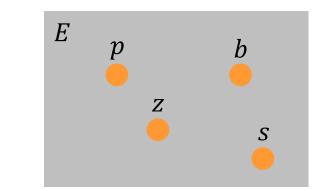
• Example:

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$$A = \{e_1, e_2\}$$

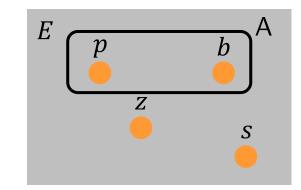


σ (e)	x ₁	x ₀
Zürich	0	0
Sydney	0	1
Beijing	1	0
Paris	1	1



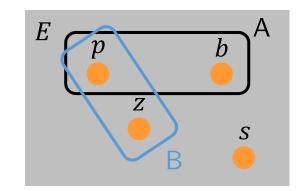
Capitals? $\psi_A(x_1, x_0) = ?$ European cities? $\psi_B(x_1, x_0) = ?$ European capitals? $\psi_c(x_1, x_0) = ?$

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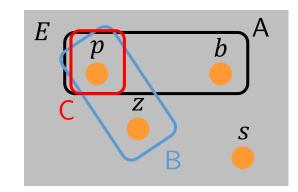
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Capitals? $\psi_A(x_1, x_0) = ?$ $\psi_A(x_1, x_0) = x_1$ European cities? $\psi_B(x_1, x_0) = ?$ $\psi_B(x_1, x_0) = \overline{x_0} \cdot \overline{x_1} + x_0 \cdot x_1$ European capitals? $\psi_c(x_1, x_0) = ?$ $C = A \cap B$ $\psi_c(x_1, x_0) = x_0 \cdot x_1$

Selecting a "good" encoding is both important and difficult

For a state space encoded with *N* bits Represent up to 2^N states

In previous example

Subset A of all capitals is represented by $\psi_A = x_1$

- No need to iterate through all capitals to verify that some property holds (e.g. "All capitals have a parliament.")
- We can use the (compact) representation of the set.

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- We can use the (compact) representation of the set.

But...

Selecting a good encoding —Representing state efficiently is difficult in practice.

It is one challenge of ML: How to efficiently encode the inputs?

Efficient state representation

Set of states as Boolean function

Binary Decision Diagram representation

Computing reachability

- Leverage efficient state representation
- Explore successor sets of states

Proving properties

- Temporal logic (CTL)
- Encoding as reachability problem

Sets and Relations using BDDs

- Representation of a relation $R \subseteq A \times B$
 - Binary encoding $\sigma(a)$, $\sigma(b)$ of all elements $a \in A$, $b \in B$
 - Representation of *R*

$$(a,b) \in R \Leftrightarrow \psi_R(\sigma(a),\sigma(b))$$

characteristic function of the relation R

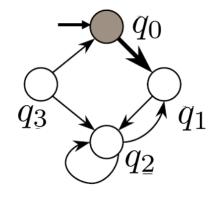
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 $(a,b) \in R \Leftrightarrow \psi_R(\sigma(a),\sigma(b))$

characteristic function of the relation R

• Example:



 $\psi_{\delta}(\sigma(q), \sigma(q')) = \psi_{\delta}(q, q')$ — To simplify notation

$$\overrightarrow{q} \xrightarrow{\delta} \overrightarrow{q'}$$

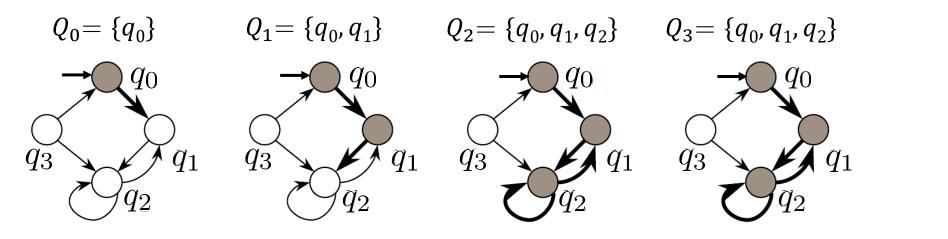
describe state transitions return 1 if there is a transition $q \rightarrow q'$, 0 otherwise

$$\psi_{\delta}(q_0,q_1)=1$$

 $\psi_{\delta}(q_0,q_3)=0$ 68

Reachability of States

- Problem: Is a state $q \in Q$ reachable by a sequence of state transitions?
- Method:
 - Represent set of states and the transformation relation as ROBDDs.
 - Use these representations to transform from one set of states to another. Set Q_i corresponds to the set of states reachable after i transitions.
 - Iterate the transformation until a fixed-point is reached, i.e., until the set of states does not change anymore (steady-state).
- Example:



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Drawing state-diagrams is not feasible in general.

Drawing state-diagrams is not feasible in general.

- 1. Work with sets of states
- 2. Use characteristic functions to represent sets of states
- 3. Use ROBDDs to encode characteristic functions

Reachability of States

• Transformation of sets of states:

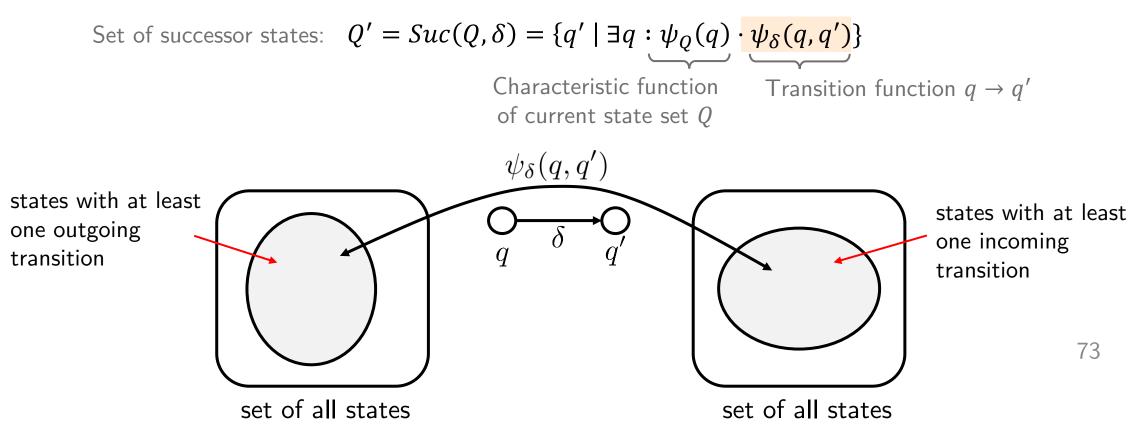
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• Determine the set of all direct successor states of a given set of states Q by means of the transformation function δ :

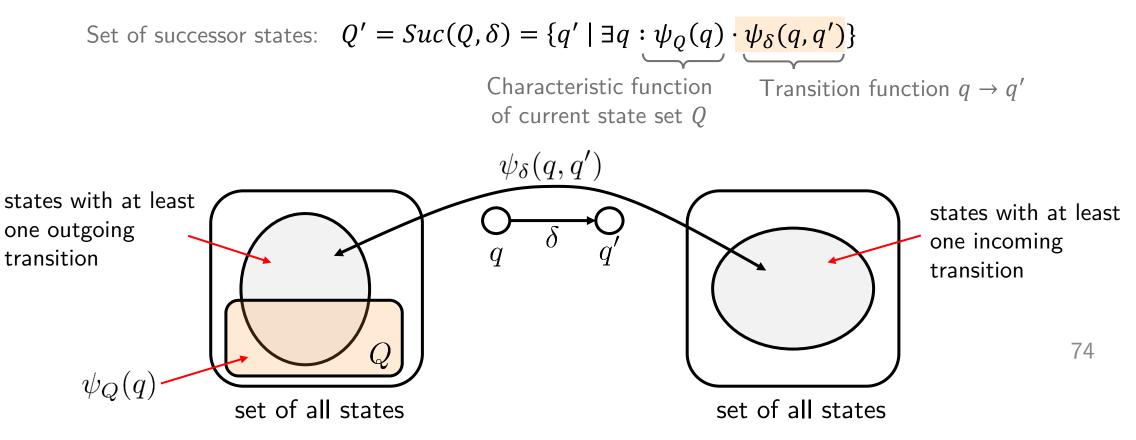
Set of successor states: $Q' = Suc(Q, \delta) = \{q' \mid \exists q : \psi_Q(q) \cdot \underbrace{\psi_\delta(q, q')}_{\text{Transition function } q \to q'$ Characteristic function function $q \to q'$

$$\begin{array}{c} & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & &$$

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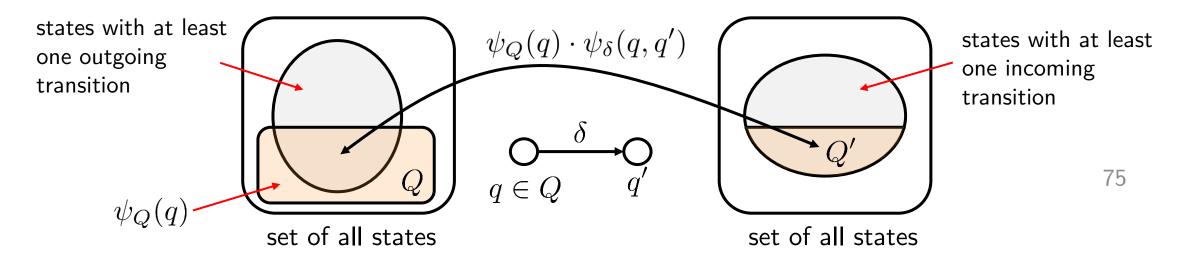


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- Transformation of sets of states:
 - Determine the set of all direct successor states of a given set of states Q by means of the transformation function δ :

Set of successor states: $Q' = Suc(Q, \delta) = \{q' \mid \exists q : \psi_Q(q) : \psi_{\delta}(q, q')\}$ Characteristic function of current state set QTransition function $q \to q'$



- Transformation of sets of states:
 - Determine the set of all direct successor states of a given set of states Q by means of the transformation function δ :

Set of successor states: $Q' = Suc(Q, \delta) = \{q' \mid \exists q : \psi_Q(q) \cdot \psi_\delta(q, q')\}$ Efficient to compute with ROBDDs $h(q, q') = \psi_Q(q) \cdot \psi_\delta(q, q')$ $\psi_{Q'}(q') = (\exists q : h(q, q'))$

• Fixed-point iteration

 $\widetilde{q_3}$

• Start with the initial state, then determine the set of states that can be reached in one or more steps.

Characteristic function of
next set of reached states
$$Q_{0} = \{q_{0}\}$$

$$Q_{i+1} = Q_{i} \cup Suc(Q_{i}, \delta) \qquad \text{until } Q_{i+1} = Q_{i}$$

$$\psi_{Q_{i+1}}(q') = \psi_{Q_{i}}(q') + (\exists q : \psi_{Q_{i}}(q) \cdot \psi_{\delta}(q, q'))$$

$$q' \text{ is already in } Q_{i} \qquad \text{There is a state } q \text{ in } Q_{i} \text{ with } transition \ q \to q'$$

$$Q_{0} = \{q_{0}\}$$

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$$Q_{0} = \{q_{0}\}$$

$$Q_{1} = Q_{0} \cup Suc(Q_{0}, \delta) = \{q_{0}, q_{1}\}$$

- Fixed-point iteration
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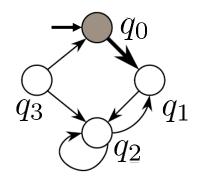
$$q' \text{ is already in } Q_{i} \qquad \text{There is a state } q \text{ in } Q_{i} \text{ with transition } q \to q'$$

Characteristic function of next set of reached states

- Due to the finite number of states, the fixed-point exists and is reached in a finite number of steps (at most the diameter of the state diagram).
- Determine whether the fixed-point is reached or not can be done by comparing the ROBDDs of the current set of reachable states. 78

$\sigma(q)$	x ₁	x ₀
q ₀	0	0
q_1	0	1
q_2	1	0
q ₃	1	1

State encoding $(x_1, x_0) = \sigma(q)$



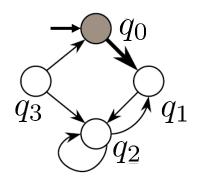
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Transition relation encoding $\psi_{\delta}(q,q')$

As a Boolean function

$$\psi_{\delta}(q,q') = \overline{x_0'} \cdot (x_0 \cdot (x_1 + x_1') + x_1 \cdot x_1') + \overline{x_0} \cdot x_0' \cdot \overline{x_1'}$$



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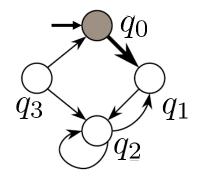
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Compute reachable states:

$$\psi_{Q_{i+1}}(q') = \psi_{Q_i}(q') + (\exists q : \psi_{Q_i}(q) \cdot \psi_{\delta}(q,q'))$$



$\sigma(q)$	x ₁	x ₀
\mathbf{q}_0	0	0
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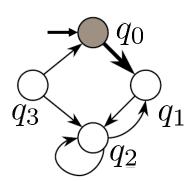
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$$Q_0 = \{q_0\}$$

$$\psi_{Q_0}(q) = \overline{x_1} \cdot \overline{x_0}$$



$\sigma(q)$	x_1	x ₀
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 $\psi_{\delta}(q,q') = \overline{x_0'} \cdot (x_0 \cdot (x_1 + x_1') + x_1 \cdot x_1') + \overline{x_0} \cdot x_0' \cdot \overline{x_1'}$

Compute reachable states: $\psi_{Q_{i+1}}(q') = \psi_{Q_i}(q') + (\exists q : \psi_{Q_i}(q) \cdot \psi_{\delta}(q,q'))$

$$Q_{1} = Q_{0} \cup \{q_{1}\}$$

= $\{q_{0}, q_{1}\}$
 q_{3} q_{1}

$$\psi_{Q_0}(q) = \overline{x_1} \cdot \overline{x_0}$$

$$\psi_{Q_1}(q') = \overline{x_1'} \cdot \overline{x_0'} + (\exists q : \overline{x_1} \cdot \overline{x_0} \cdot \psi_{\delta}(q, q'))$$

$\sigma(q)$	x ₁	x ₀
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= $\{q_{0}, q_{1}\}$
 q_{3}
 q_{2}

$$\psi_{Q_0}(q) = \overline{x_1} \cdot \overline{x_0}$$

$$\psi_{Q_1}(q') = \overline{x_1'} \cdot \overline{x_0'} + (\exists q : \overline{x_1} \cdot \overline{x_0} \cdot \psi_{\delta}(q, q'))$$

$$q_0: x_0 = 0, x_1 = 0$$

$$= \overline{x_1'} \cdot \overline{x_0'} + \overline{x_1'} \cdot x_0' = \overline{x_1'}$$
From the second se

From BDDs and quantifiers: $\exists x: f = f \Big|_{x=0} + f \Big|_{x=1}$ The only non-zero term is for $x_0=0, x_1=0$ (see next slide)

Reachability of States – Example (BDD Calculation) $\psi_{Q_{i+1}}(q') = \psi_{Q_i}(q') + (\exists q : \psi_{Q_i}(q) \cdot \psi_{\delta}(q, q'))$ $\psi_{Q_0}(q) = \overline{x_1} \cdot \overline{x_0}$ $Eq_1: \psi_{Q_1}(q') = \overline{x'_1} \cdot \overline{x'_0} + (\exists q : \overline{x_1} \cdot \overline{x_0} \cdot \psi_{\delta}(q, q'))$ $\exists q: f \to \exists x_1 \exists x_0: f$ From BDDs and quantifiers: $(\exists x_1, x_2: f) \Leftrightarrow (\exists x_1 (\exists x_2: f)))$ $\exists x: f = f \Big|_{x=0} + f \Big|_{x=1}$

$$\exists x_0: f \qquad f \Big|_{x_0=1} = \overline{x_1} \cdot 0 \cdot (\overline{x_0'} \cdot (1 \cdot (x_1 + x_1') + x_1 \cdot x_1') + 0 \cdot x_0' \cdot \overline{x_1'}) = 0$$

$$f \Big|_{x_0=0} = \overline{x_1} \cdot 1 \cdot (\overline{x_0'} \cdot (0 \cdot (x_1 + x_1') + x_1 \cdot x_1') + 1 \cdot x_0' \cdot \overline{x_1'}) = \overline{x_1} \cdot (\overline{x_0'} \cdot (x_1 \cdot x_1') + x_0' \cdot \overline{x_1'})$$

$$\exists x_1: f \Big|_{x_0=0} \quad f|_{x_0=0,x_1=1} = 0 \cdot (\overline{x_0'} \cdot (1 \cdot x_1') + x_0' \cdot \overline{x_1'}) = 0$$
$$f|_{x_0=0,x_1=0} = 1 \cdot (\overline{x_0'} \cdot (0 \cdot x_1') + x_0' \cdot \overline{x_1'}) = x_0' \cdot \overline{x_1'}$$

 $\exists x_1 \exists x_0 : f = x'_0 \cdot \overline{x_1}'$ — Plug into Eq₁ to compute $\psi_{Q_1}(q')$

$\sigma(q)$	x ₁	x ₀
\mathbf{q}_0	0	0
q_1	0	1
q_2	1	0
q ₃	1	1

State encoding $(x_1, x_0) = \sigma(q)$

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 $\psi_{\delta}(q,q') = \overline{x_0'} \cdot (x_0 \cdot (x_1 + x_1') + x_1 \cdot x_1') + \overline{x_0} \cdot x_0' \cdot \overline{x_1'}$

Compute reachable states: $\psi_{Q_{i+1}}(q') = \psi_{Q_i}(q') + (\exists q : \psi_{Q_i}(q) \cdot \psi_{\delta}(q,q'))$

 $Q_{2} = Q_{1} \cup \{q_{1}, q_{2}\}$ = $\{q_{0}, q_{1}, q_{2}\}$ q_{3} q_{2} q_{2}

$$\psi_{Q_1}(q') = \overline{x'_1}$$

$$\psi_{Q_2}(q') = \overline{x'_1} + (\exists q : \overline{x_1} \cdot \psi_{\delta}(q, q'))$$

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$$q_1: x_0 = x_1 + x_1' \cdot x_0' = x_1' + x_0'$$

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 $Q_{3} = Q_{2} \cup \{q_{1}, q_{2}\}$ = $\{q_{0}, q_{1}, q_{2}\}$ q_{3} q_{3} q_{2} Transition relation encoding $\psi_{\delta}(q,q')$

As a Boolean function

$$\psi_{\delta}(q,q') = \overline{x_0'} \cdot (x_0 \cdot (x_1 + x_1') + x_1 \cdot x_1') + \overline{x_0} \cdot x_0' \cdot \overline{x_1'}$$

Compute reachable states: $\psi_{Q_{i+1}}(q') = \psi_{Q_i}(q') + (\exists q : \psi_{Q_i}(q) \cdot \psi_{\delta}(q,q'))$

$$\psi_{Q_2}(q') = \overline{x_1'} + \overline{x_0'}$$

$$\psi_{Q_3}(q') = \overline{x_1'} + \overline{x_0'} + (\exists q : (\overline{x_1} + \overline{x_0}) \cdot \psi_{\delta}(q, q'))$$

$$q_0, q_1, q_2$$

$$= \overline{x_1'} + \overline{x_0'} + \overline{x_1'} + \overline{x_0'} = \overline{x_1'} + \overline{x_0'}$$

It's always a reachability problem

Or rather

The goal is to transform the problem at hand to **encode it as a reachability problem**.

B

Because these can be solved very efficiently

- 1. Work with sets of states
- 2. Use characteristic functions to represent sets of states
- 3. Use ROBDDs to encode characteristic functions

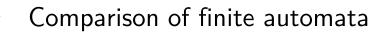
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Or rather

The goal is to transform the problem at hand to **encode it as a reachability problem**.

Because these can be solved very efficiently

- 1. Work with sets of states
- 2. Use characteristic functions to represent sets of states
- 3. Use ROBDDs to encode characteristic functions



- 1. Compute the set of jointly reachable states
- 2. Compare the output values of two finite automata

3. ...

Your turn to practice! after the break

- Familiarise yourself with the equivalence "set of states" ≡ "characteristic functions"
- 2. Express system properties using characteristic functions
- 3. Draw and simplify BDDs to compare a specification and an implementation

Efficient state representation

Computing reachability

- Set of states as Boolean function
- Binary Decision Diagram representation

- Leverage efficient state representation
- Explore successor sets of states



- Temporal logic (CTL)
- Encoding as reachability problem