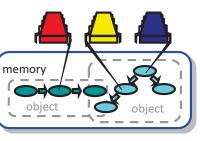
# Small Systems Chapter 8



#### Concurrent Computation

- We started with...
- Multiple threads
  - Sometimes called processes
- Single shared memory
- Objects live in memory
- Unpredictable asynchronous delays
- In the previous chapters, we focused on fault-tolerance
  - We discussed theoretical results
  - We discussed practical solutions with a focus on efficiency
- In this chapter, we focus on efficient concurrent computation!
  - Focus on asynchrony and not on explicit failures



#### Overview

- Introduction
- Spin Locks
  - Test-and-Set & Test-and-Test-and-Set
  - Backoff lock
  - Queue locks
- Concurrent Linked List
  - Fine-grained synchronization
  - Optimistic synchronization
  - Lazy synchronization
  - Lock-free synchronization
- Hashing
  - Fine-grained locking
  - Recursive split ordering

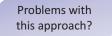
Distributed Systems – Roger Wattenhofer – 8/2

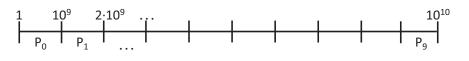
## **Example: Parallel Primality Testing**

- Challenge
  - Print all primes from 1 to 10<sup>10</sup>
- Given
  - Ten-core multiprocessor
  - One thread per processor
- Goal
  - Get ten-fold speedup (or close)
- Naïve Approach

Split the work evenly

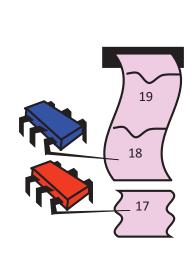
- Each thread tests range of  $10^9$ 





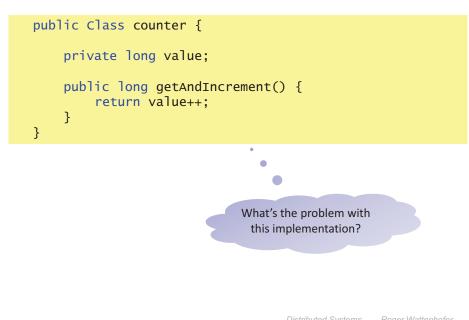


- Higher ranges have fewer primes
- Yet larger numbers harder to test
- Thread workloads
  - Uneven
  - Hard to predict
- Need dynamic load balancing
- Better approach
  - Shared counter!
  - Each thread takes a number

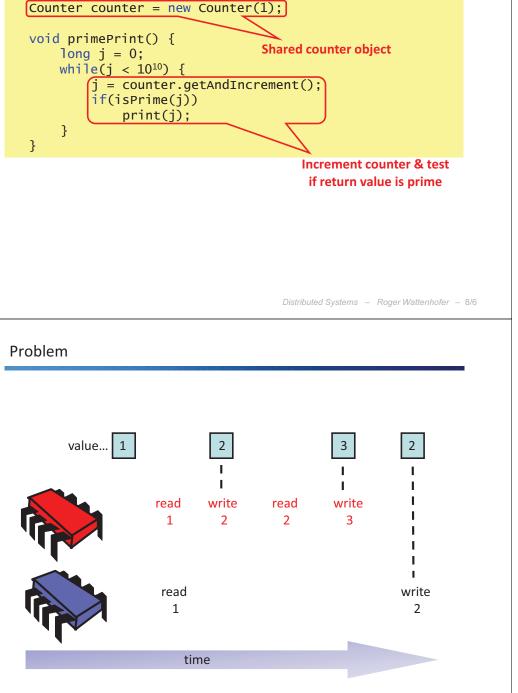


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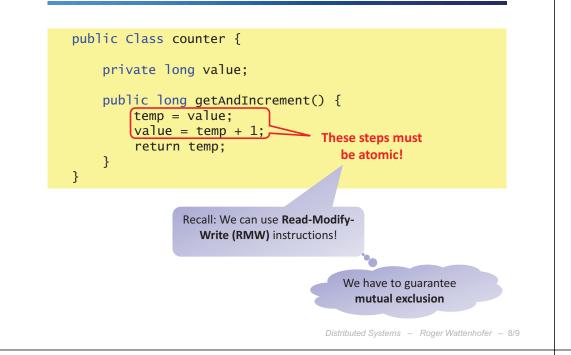
#### Counter Implementation



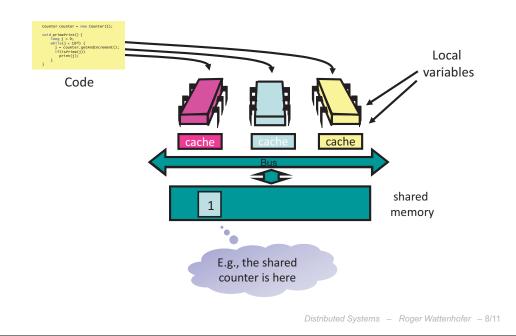
#### Procedure Executed at each Thread



#### Counter Implementation



# Model: Where Things Reside

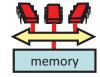


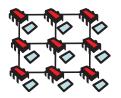
#### Model

- The model in this part is slightly more complicated
  - However, we still focus on principles

#### I.e., multiprocessors

- What remains the same?
  - Multiple instruction multiple data (MIMD) architecture
  - Each thread/process has its own code and local variables
  - There is a shared memory that all threads can access
- What is new?
  - Typically, communication runs over a shared bus (alternatively, there may be several channels)
  - Communication contention
  - Communication latency
  - Each thread has a local cache



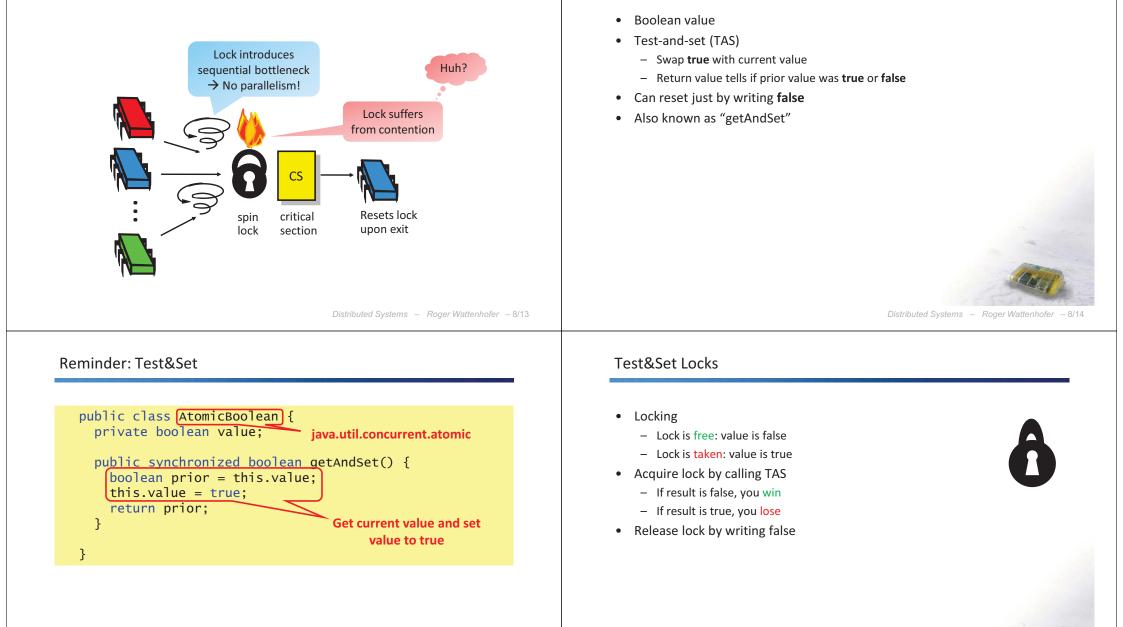


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# **Revisiting Mutual Exclusion**

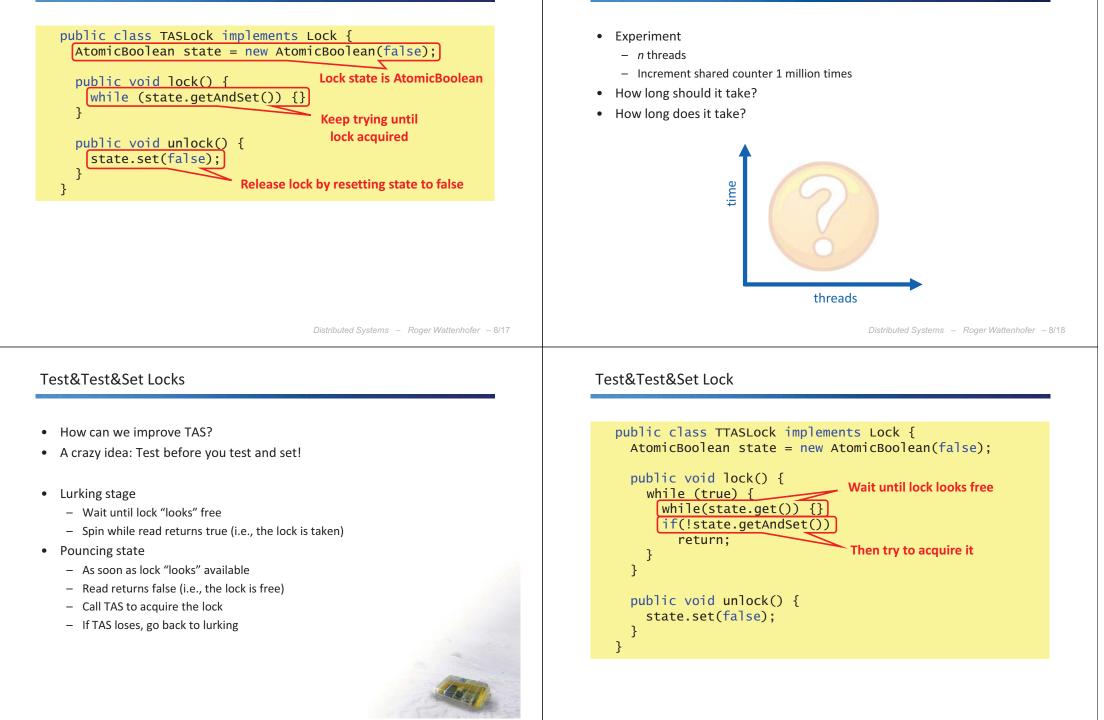
- We need mutual exclusion for our counter
- We are now going to study mutual exclusion from a different angle
  - Focus on performance, not just correctness and progress
- We will begin to understand how performance depends on our software properly utilizing the multiprocessor machine's hardware, and get to know a collection of locking algorithms!
- What should you do if you can't get a lock?
- Keep trying
  - "spin" or "busy-wait"
  - Good if delays are short
- ≻ Our focus
- Give up the processor
  - Good if delays are long
  - Always good on uniprocessor





Reminder: Test&Set

#### Test&Set Lock



Performance

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#### Performance

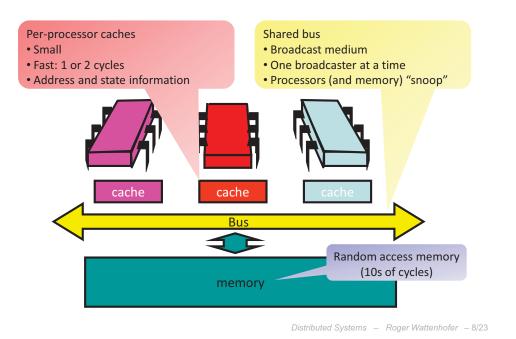
- Both TAS and TTAS do the same thing (in our old model)
- So, we would expect basically the same results

# TAS lock TTAS lock ideal threads

• Why is TTAS so much better than TAS? Why are both far from ideal?

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# **Bus-Based Architectures**



#### Opinion

- TAS & TTAS locks
  - are provably the same (in our old model)
  - except they aren't (in field tests)
- Obviously, it must have something to do with the model...
- Let's take a closer look at our new model and try to find a reasonable explanation!



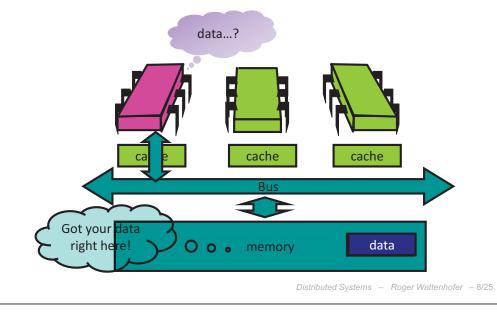
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## Jargon Watch

- Load request
  - When a thread wants to access data, it issues a load request
- Cache hit
  - The thread found the data in its own cache
- Cache miss
  - The data is not found in the cache
  - The thread has to get the data from memory

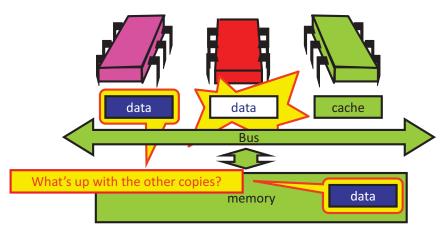
#### Load Request

• Thread issues load request and memory responds



#### Modify Cached Data

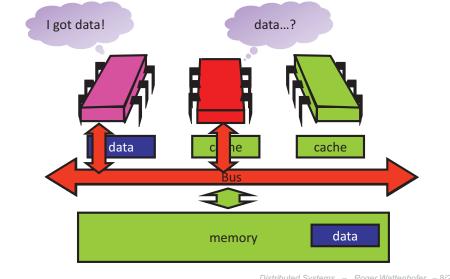
- Both threads now have the data in their cache •
- What happens if the red thread now modifies the data...?



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#### Another Load Request

• Another thread wants to access the same data. Get a copy from the cache!



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## **Cache Coherence**

- We have lots of copies of data
  - Original copy in memory
  - Cached copies at processors
- Some processor modifies its own copy
  - What do we do with the others?
  - How to avoid confusion?

#### Write-Back Caches

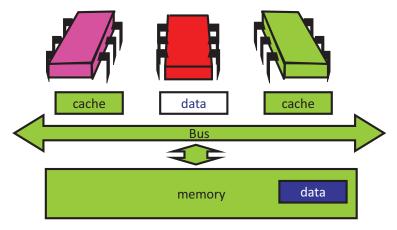
- Accumulate changes in cache
- Write back when needed
  - Need the cache for something else
  - Another processor wants it
- On first modification
  - Invalidate other entries
  - Requires non-trivial protocol ...
- Cache entry has three states:
- Invalid: contains raw bits
- Valid: I can read but I can't write
- Dirty: Data has been modified
  - Intercept other load requests
  - Write back to memory before reusing cache



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#### Invalidate

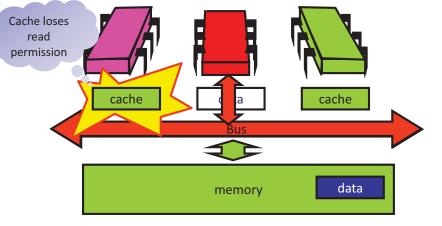
- Memory provides data only if not present in any cache, so there is no need to change it now (this is an expensive operation!)
- Reading is not a problem ightarrow The threads get the data from the red process



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#### Invalidate

- Let's rewind back to the moment when the red processor updates its cached data
- It broadcasts an invalidation message → Other processor invalidates its cache!



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# **Mutual Exclusion**

- What do we want to optimize?
  - 1. Minimize the bus bandwidth that the spinning threads use
  - 2. Minimize the lock acquire/release latency
  - 3. Minimize the latency to acquire the lock if the lock is idle



## TAS vs. TTAS

- TAS invalidates cache lines
- Spinners
  - Miss in cache
  - Go to bus
- Thread wants to release lock
  - delayed behind spinners!!!
- TTAS waits until lock "looks" free
  - Spin on local cache
  - No bus use while lock busy
- Problem: when lock is released
  - Invalidation storm .....



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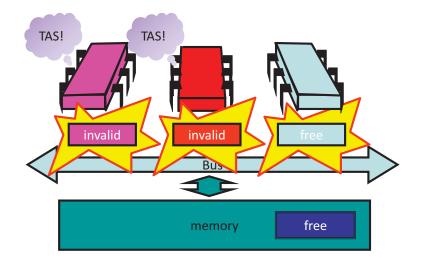
This is why TAS

performs so poorly...

#### On Release

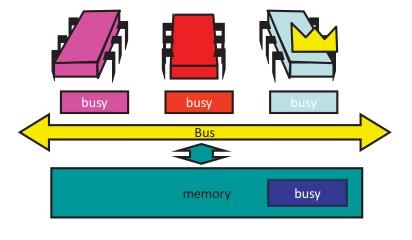
• The lock is released. All spinners take a cache hit and call Test&Set!

Huh?



# Local Spinning while Lock is Busy

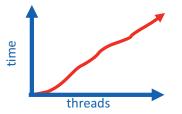
• While the lock is held, all contenders spin in their caches, rereading cached data without causing any bus traffic

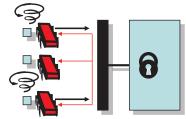


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## Time to Quiescence

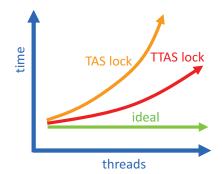
- Every process experiences a cache miss
  - All state.get() satisfied sequentially
- Every process does TAS
  - Caches of other processes are invalidated
- Eventual quiescence ("silence") after acquiring the lock
- The time to quiescence increases linearly with the number of processors for a bus architecture!





#### **Mystery Explained**

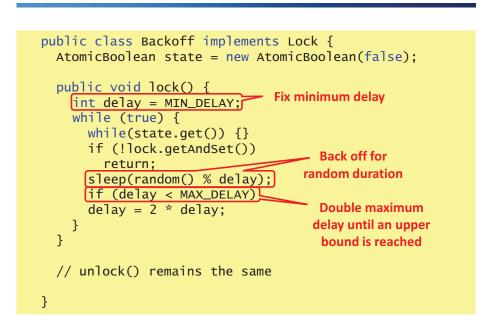
• Now we understand why the TTAS lock performs much better than the TAS lock, but still much worse than an ideal lock!



• How can we do better?

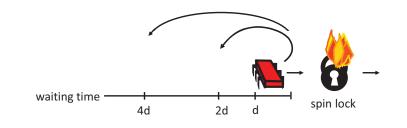
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Exponential Backoff Lock



#### Introduce Delay

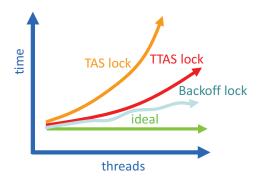
- If the lock looks free, but I fail to get it, there must be lots of contention
- It's better to back off than to collide again!
- Example: Exponential Backoff
- Each subsequent failure doubles expected waiting time



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## Backoff Lock: Performance

- The backoff log outperforms the TTAS lock!
- But it is still not ideal...



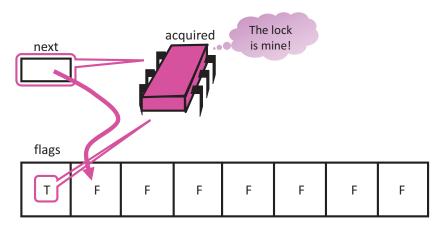
## Backoff Lock: Evaluation

- Good
  - Easy to implement
  - Beats TTAS lock
- Bad
  - Must choose parameters carefully
  - Not portable across platforms
- How can we do better?
- Avoid useless invalidations
  - By keeping a queue of threads
- Each thread
  - Notifies next in line
  - Without bothering the others

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# ALock: Acquiring the Lock

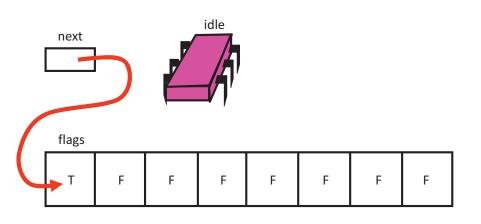
- To acquire the lock, each thread atomically increments the tail field
- If the flag is true, the lock is acquired
- Otherwise, spin until the flag is true



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## ALock: Initially

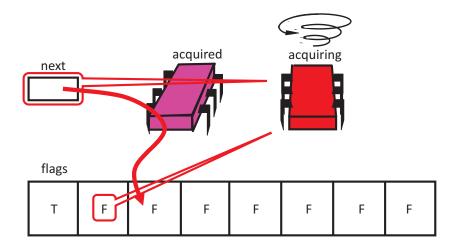
- The Anderson queue lock (ALock) is an array-based queue lock
- Threads share an atomic tail field (called next)



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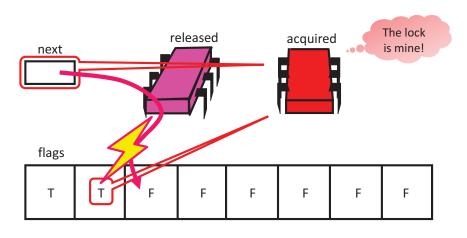
# ALock: Contention

- If another thread wants to acquire the lock, it applies get&increment
- The thread spins because the flag is false



## ALock: Releasing the Lock

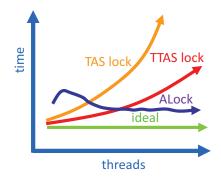
- The first thread releases the lock by setting the next slot to true
- The second thread notices the change and gets the lock



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# ALock: Performance

- Shorter handover than backoff
- Curve is practically flat
- Scalable performance
- FIFO fairness

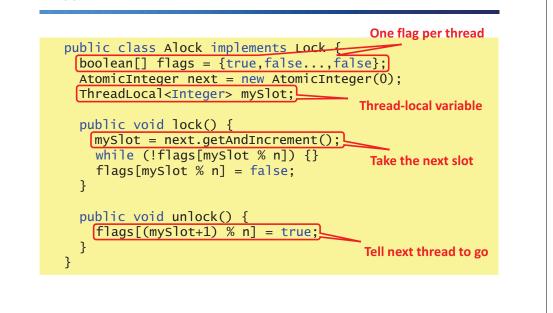




• Good

ALock

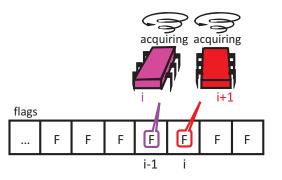
- First truly scalable lock
- Simple, easy to implement
- Bad
  - One bit per thread
  - Unknown number of threads?



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#### ALock: Alternative Technique

• The threads could update own flag and spin on their predecessor's flag



 This is basically what the CLH lock does, but using a linked list instead of an array

• Is this a good idea?

Not discussed in this lecture

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# MCS Lock

#### • Idea

- Use a linked list instead of an array
  - $\rightarrow$  Small, constant-sized space
- Spin on own flag, just like the Anderson queue lock
- The space usage
  - L = number of locks
  - N = number of threads
- of the Anderson lock is O(LN)
- of the MCS lock is O(L+N)



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#### **NUMA** Architectures

- Non-Uniform Memory Architecture
- Illusion
  - Flat shared memory
- Truth
  - No caches (sometimes)
  - Some memory regions faster than others

Spinning on local memory is fast:

Spinning on remote memory is slow:

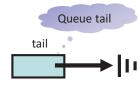


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# MCS Lock: Initially

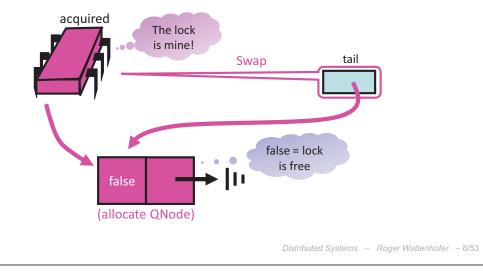
- The lock is represented as a linked list of QNodes, one per thread
- The tail of the queue is shared among all threads





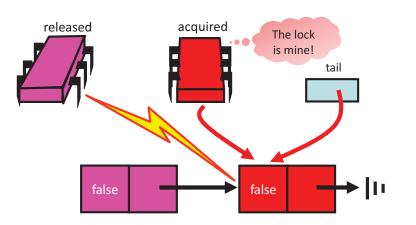
## MCS Lock: Acquiring the Lock

- To acquire the lock, the thread places its QNode at the tail of the list by swapping the tail to its QNode
- If there is no predecessor, the thread acquires the lock



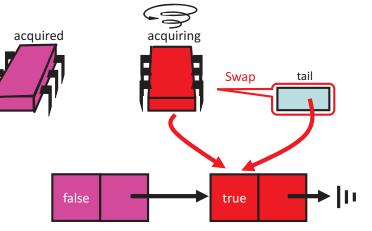
#### MCS Lock: Releasing the Lock

• The first thread releases the lock by setting its successor's QNode to false



#### MCS Lock: Contention

- If another thread wants to acquire the lock, it again applies swap
- The thread spins on its own QNode because there is a predecessor

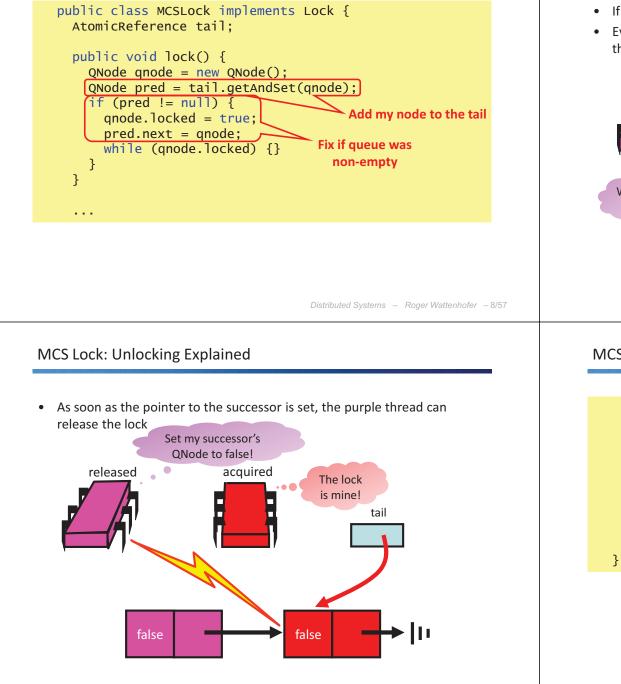


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#### MCS Queue Lock

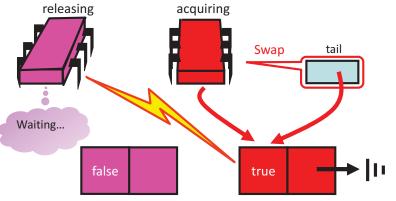
<pre>public class QNode {</pre>	
boolean locked = false;	
QNode next = null;	
}	

#### MCS Queue Lock



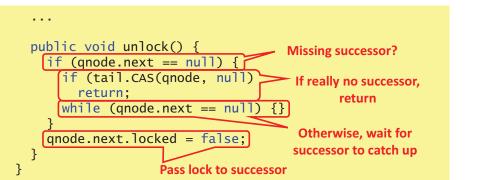
#### MCS Lock: Unlocking

- If there is a successor, unlock it. But, be cautious!
- Even though a QNode does not have a successor, the purple thread knows that another thread is active because tail does not point to its QNode!



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#### MCS Queue Lock



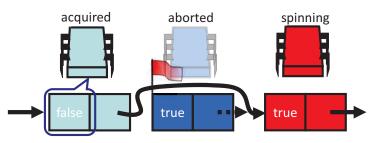
#### Abortable Locks

- What if you want to give up waiting for a lock?
- For example
  - Time-out
  - Database transaction aborted by user
- Back-off Lock
  - Aborting is trivial: Just return from lock() call!
  - Extra benefit: No cleaning up, wait-free, immediate return
- Queue Locks
  - Can't just quit: Thread in line behind will starve
  - Need a graceful way out...

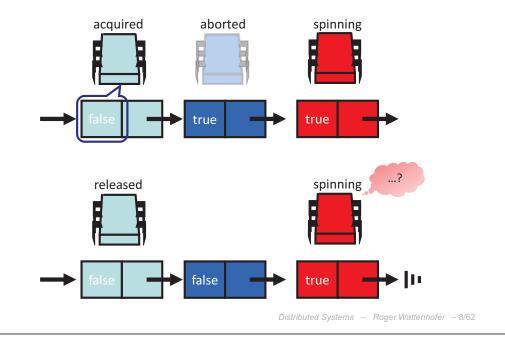
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#### Abortable MCS Lock

- A mechanism is required to recognize and remove aborted threads
  - A thread can set a flag indicating that it aborted
  - The predecessor can test if the flag is set • •
- Spinning on remote object...?!
- If the flag is set, its new successor is the successor's successor
- How can we handle concurrent aborts? This is not discussed in this lecture



## Problem with Queue Locks



## **Composite Locks**

- Queue locks have many advantages
  - FIFO fairness, fast lock release, low contention

but require non-trivial protocols to handle aborts (and recycling of nodes)

- Backoff locks support trivial time-out protocols but are not scalable and may have slow lock release times
- A composite lock combines the best of both approaches!
- Short fixed-sized array of lock nodes
- Threads randomly pick a node and try to acquire it
- Use backoff mechanism to acquire a node
- Nodes build a queue
- Use a queue lock mechanism to acquire the lock

#### One Lock To Rule Them All?

- TTAS+Backoff, MCS, Abortable MCS...
- Each better than others in some way
- There is not a single best solution
- Lock we pick really depends on
  - the application
  - the hardware
  - which properties are important



- Adding threads should not lower the throughput
  - Contention effects can mostly be fixed by Queue locks
- Adding threads should increase throughput
  - Not possible if the code is inherently sequential
  - Surprising things are parallelizable!
- How can we guarantee consistency if there are many threads?



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#### **Coarse-Grained Synchronization**

- Each method locks the object
  - Avoid contention using queue locks
  - Mostly easy to reason about
  - This is the standard Java model (synchronized blocks and methods)
- Problem: Sequential bottleneck
  - Threads "stand in line"
  - Adding more threads does not improve throughput
  - We even struggle to keep it from getting worse...
- So why do we even use a multiprocessor?
  - Well, some applications are inherently parallel...
  - We focus on exploiting non-trivial parallelism

## **Exploiting Parallelism**

- We will now talk about four "patterns"
  - Bag of tricks ...
  - Methods that work more than once ...
- The goal of these patterns are
  - Allow concurrent access
  - If there are more threads, the throughput increases!



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### Pattern #1: Fine-Grained Synchronization

- Instead of using a single lock split the concurrent object into independently-synchronized components
- Methods conflict when they access
  - The same component
  - At the same time



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#### Pattern #3: Lazy Synchronization

- Postpone hard work!
- Removing components is tricky
  - Either remove the object physically
  - Or logically: Only mark component to be deleted



- Assume that nobody else wants to access your part of the concurrent object
- Search for the specific part that you want to lock without locking any other part on the way
- If you find it, try to lock it and perform your operations
  - If you don't get the lock, start over!
- Advantage
  - Usually cheaper than always assuming that there may be a conflict due to a concurrent access



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## Pattern #4: Lock-Free Synchronization

- Don't use locks at all!
  - Use compareAndSet() & other RMW operations!
- Advantages
  - No scheduler assumptions/support
- Disadvantages
  - Complex
  - Sometimes high overhead



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## Illustration of Patterns

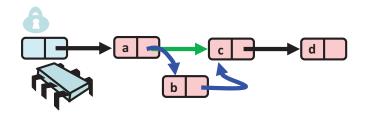
- In the following, we will illustrate these patterns using a list-based set
  - Common application
  - Building block for other apps
- A set is an collection of items
  - No duplicates
- The operations that we want to allow on the set are
  - add(x) puts x into the set
  - **remove(x)** takes **x** out of the set
  - contains (x) tests if x is in the set



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# **Coarse-Grained Locking**

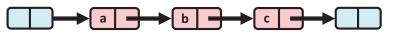
- A simple solution is to lock the entire list for each operation
  - E.g., by locking the first sentinel



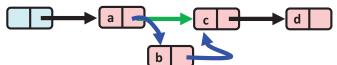
- Simple and clearly correct!
- Works poorly with contention...

The List-Based Set

• We assume that there are sentinel nodes at the beginning and end of the linked list



• Add node b:



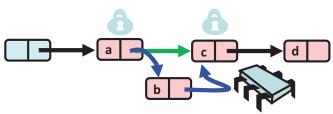
• Remove node b:



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# Fine-Grained Locking

- Split object (list) into pieces (nodes)
  - Each piece (each node in the list) has its own lock
  - Methods that work on disjoint pieces need not exclude each other

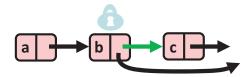


- Hand-over-hand locking: Use two locks when traversing the list
  - Why two locks?

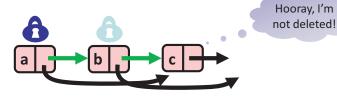


#### Problem with One Lock

- Assume that we want to delete node c •
- We lock node b and set its next pointer to the node after c •



• Another thread may concurrently delete node b by setting the next pointer from node a to node c



Insight

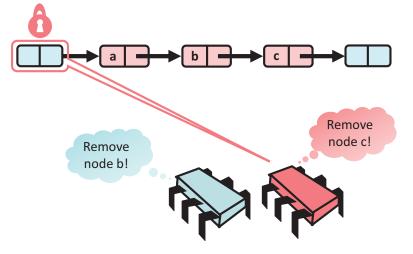
- If a node is locked, no one can delete the node's successor
- If a thread locks
  - the node to be deleted
  - and also its predecessor
- then it works!
- That's why we (have to) use two locks!



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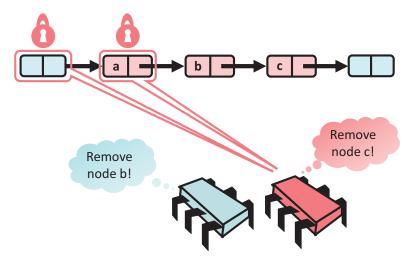
## Hand-Over-Hand Locking: Removing Nodes

- Assume that two threads want to remove the nodes b and c
- One thread acquires the lock to the sentinel, the other has to wait



# Hand-Over-Hand Locking: Removing Nodes

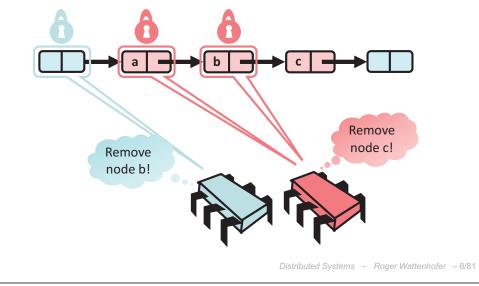
• The same thread that acquired the sentinel lock can then lock the next node



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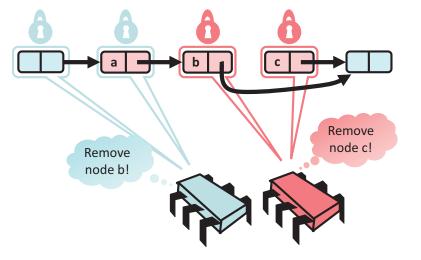
#### Hand-Over-Hand Locking: Removing Nodes

- Before locking node b, the sentinel lock is released
- The other thread can now acquire the sentinel lock



# Hand-Over-Hand Locking: Removing Nodes

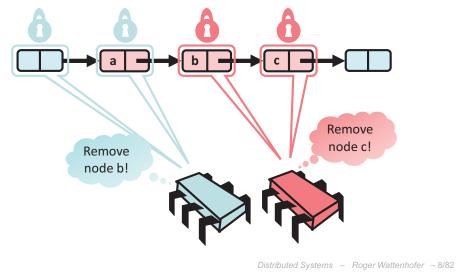
- Node c can now be removed
- Afterwards, the two locks are released



#### Distributed Systems – Roger Wattenhofer – 8/83

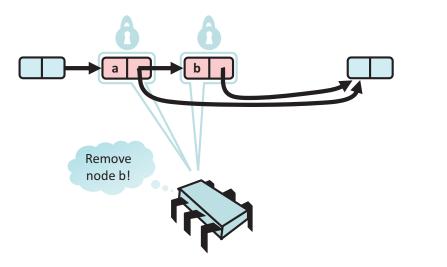
Hand-Over-Hand Locking: Removing Nodes

- Before locking node c, the lock of node a is released
- The other thread can now lock node a

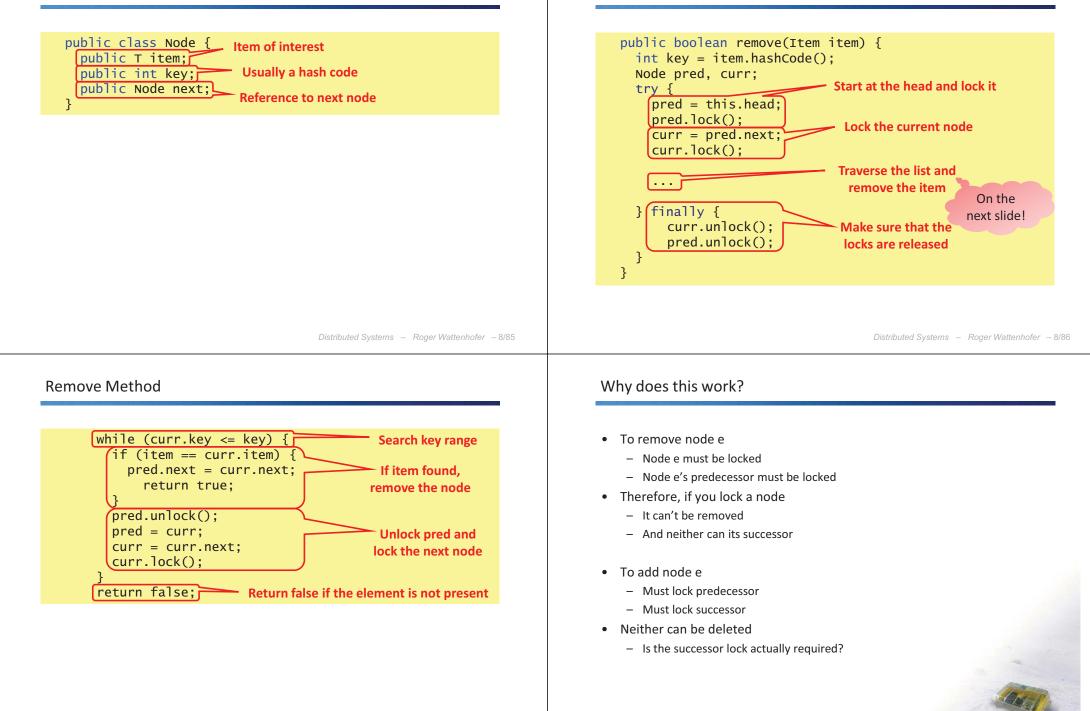


# Hand-Over-Hand Locking: Removing Nodes

• The other thread can now lock node b and remove it







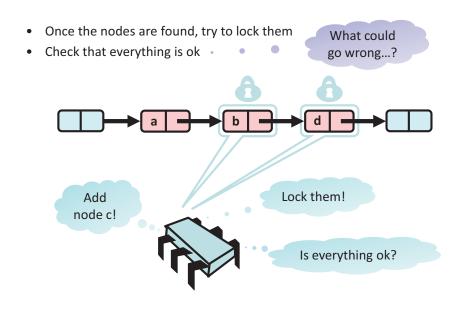
**Remove Method** 

#### Drawbacks

- Hand-over-hand locking is sometimes better than coarse-grained lock
  - Threads can traverse in parallel
  - Sometimes, it's worse!
- However, it's certainly not ideal
  - Inefficient because many locks must be acquired and released
- How can we do better?

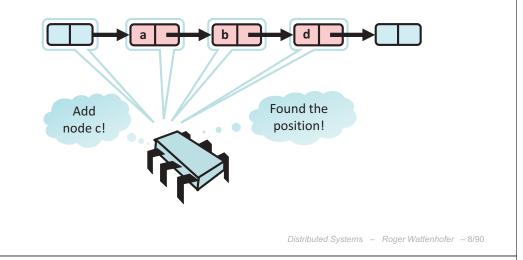
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# Optimistic Synchronization: Traverse without Locking



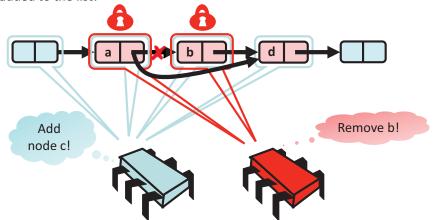
# Optimistic Synchronization

• Traverse the list without locking!



# Optimistic Synchronization: What Could Go Wrong?

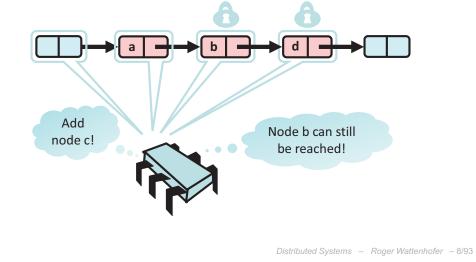
 Another thread may lock nodes a and b and remove b before node c is added → If the pointer from node b is set to node c, then node c is not added to the list!



#### Optimistic Synchronization: Validation #1

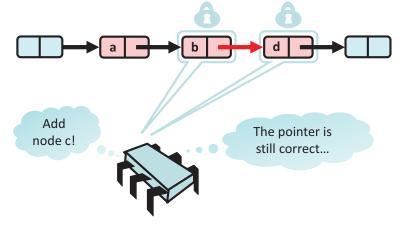
Optimistic Synchronization: What Else Could Go Wrong?

- How can this be fixed? •
- After locking node b and node d, traverse the list again to verify that b is still reachable

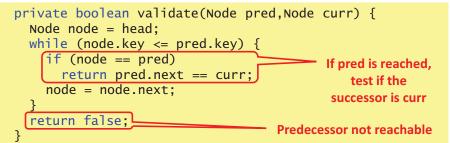


# **Optimistic Synchronization: Validation #2**

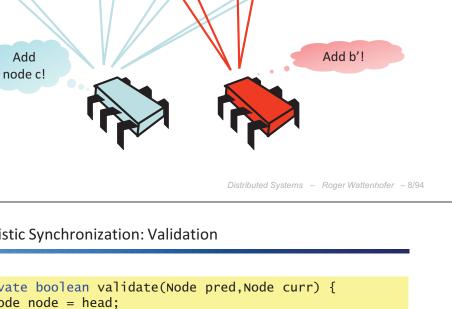
- How can this be fixed?
- After locking node b and node d, also check that node b still points to node d!



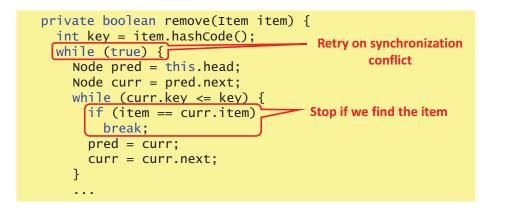
**Optimistic Synchronization: Validation** 



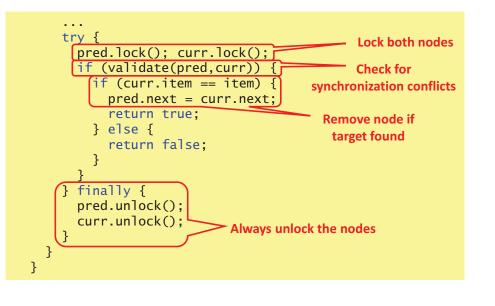
• Another thread may lock node a and b and add a node b' before node c is added  $\rightarrow$  By adding node c, the addition of node b' is undone!



#### Optimistic Synchronization: Remove



#### Optimistic Synchronization: Remove



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**Optimistic Synchronization** 

- Why is this correct?
  - If nodes b and c are both locked, node b still accessible, and node c still the successor of node b, then neither b nor c will be deleted by another thread
  - This means that it's ok to delete node c!
- Why is it good to use optimistic synchronization?
  - Limited hot-spots: no contention on traversals
  - Less lock acquisitions and releases
- When is it good to use optimistic synchronization?
  - When the cost of scanning twice without locks is less than the cost of scanning once with locks
- Can we do better?
  - It would be better to traverse the list only once...

## Lazy Synchronization

- Key insight
  - Removing nodes causes trouble
  - Do it "lazily"
- How can we remove nodes "lazily"?
  - First perform a logical delete: Mark current node as removed (new!)



- Then perform a physical delete: Redirect predecessor's next (as before)



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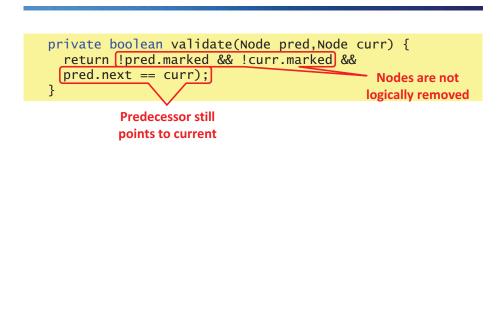
#### Lazy Synchronization

- All Methods
  - Scan through locked and marked nodes
  - Removing a node doesn't slow down other method calls...
- Note that we must still lock pred and curr nodes!
- How does validation work?
  - Check that neither pred nor curr are marked
  - Check that pred points to curr



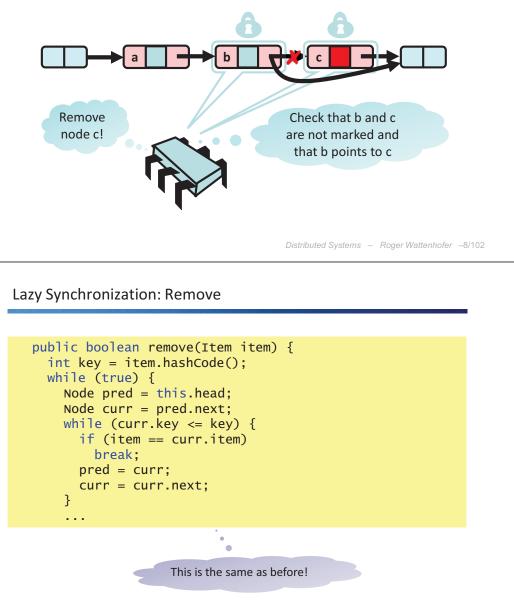
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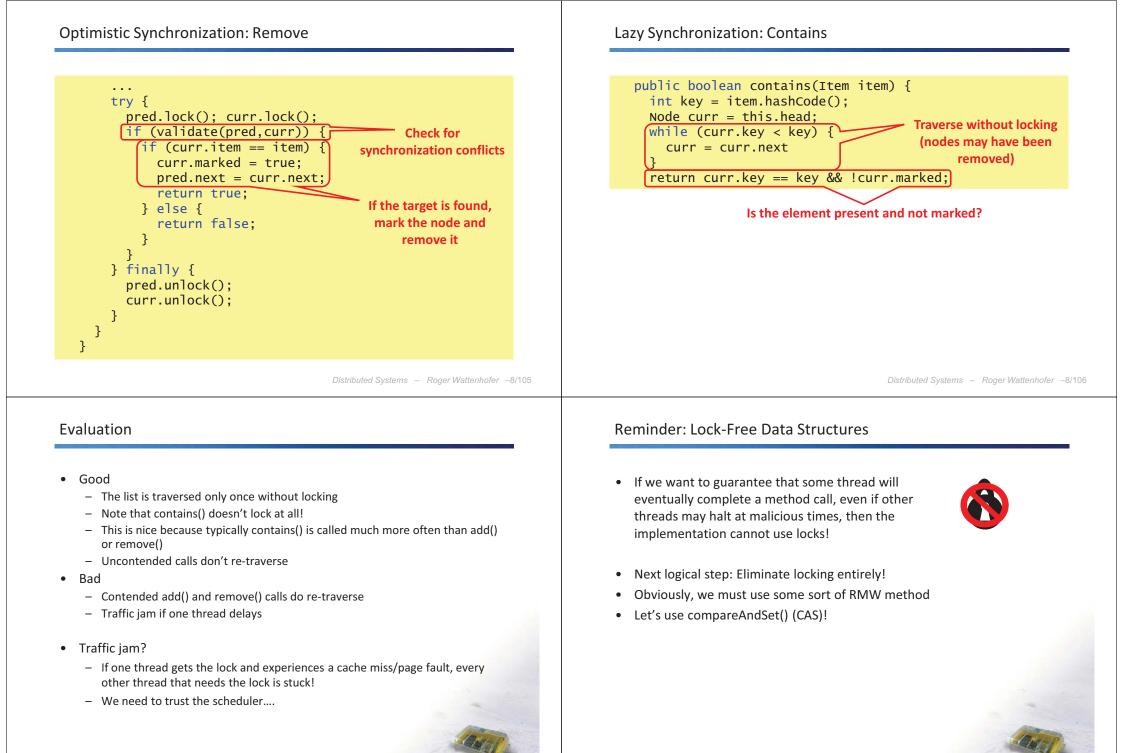
#### Lazy Synchronization: Validation



Lazy Synchronization

- Traverse the list and then try to lock the two nodes
- Validate!
- Then, mark node c and change the predecessor's next pointer



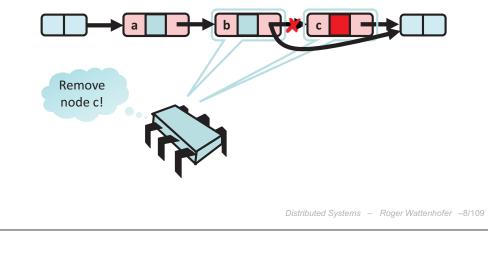


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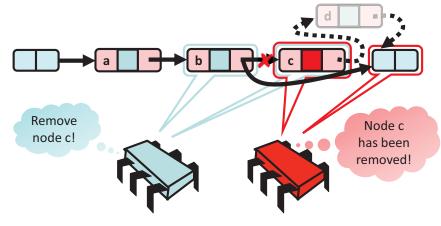
## **Remove Using CAS**

- First, remove the node logically (i.e., mark it)
- Then, use CAS to change the next pointer
- Does this work...?



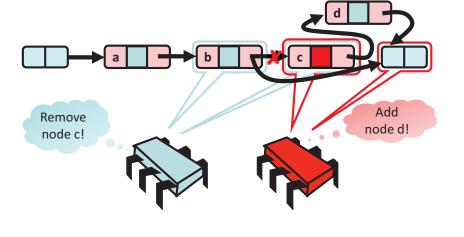
## Solution

- Mark bit and next pointer are "CASed together"
- This atomic operation ensures that no node can cause a conflict by adding (or removing) a node at the same position in the list



#### Remove Using CAS: Problem

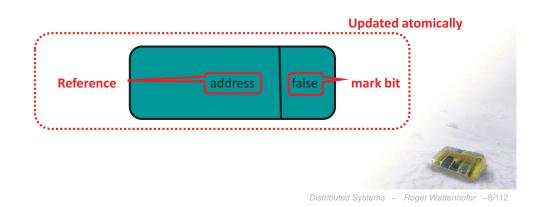
- Unfortunately, this doesn't work!
- Another node d may be added before node c is physically removed
- As a result, node d is not added to the list...



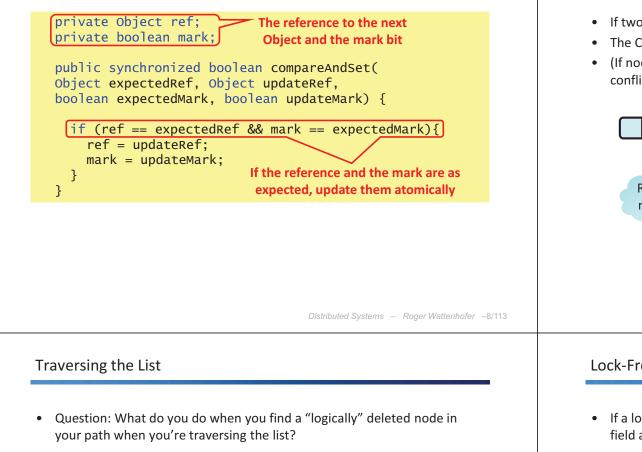
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#### Solution

- Such an operation is called an atomic markable reference
  - Atomically update the mark bit and redirect the predecessor's next pointer
- In Java, there's an AtomicMarkableReference class
  - In the package Java.util.concurrent.atomic package



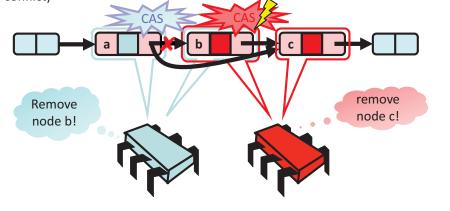
#### **Changing State**





#### **Removing a Node**

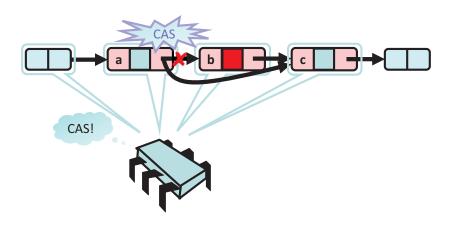
- If two threads want to delete the nodes b and c, both b and c are marked
- The CAS of the red thread fails because node b is marked!
- (If node b is yet not marked, then b is removed first and there is no conflict)



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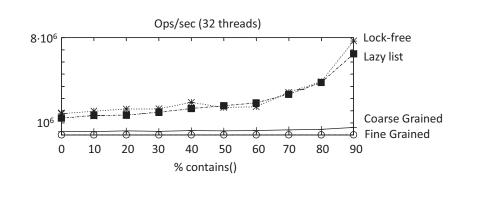
## Lock-Free Traversal

• If a logically deleted node is encountered, CAS the predecessor's next field and proceed (repeat as needed)



#### Performance

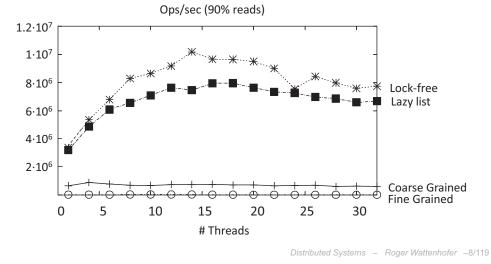
- The throughput of the presented techniques has been measured for a varying percentage of contains() method calls
  - Using a benchmark on a 16 node shared memory machine



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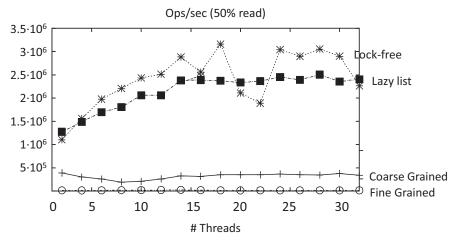
# High Ratio of contains()

• If the ratio of contains() is high, again both the lock-free linked list and the linked list with lazy synchronization perform well even if there are many threads



#### Low Ratio of contains()

• If the ratio of contains() is low, the lock-free linked list and the linked list with lazy synchronization perform well even if there are many threads



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#### "To Lock or Not to Lock"

- Locking vs. non-blocking: Extremist views on both sides
- It is nobler to compromise by combining locking and non-blocking techniques
  - Example: Linked list with lazy synchronization combines blocking add() and remove() and a non-blocking contains()
  - Blocking/non-blocking is a property of a method



#### Linear-Time Set Methods

• We looked at a number of ways to make highly-concurrent list-based sets

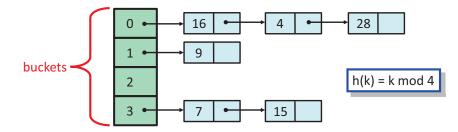
How...?

- Fine-grained locks
- Optimistic synchronization
- Lazy synchronization
- Lock-free synchronization
- What's not so great?
  - add(), remove(), contains() take time linear in the set size
- We want constant-time methods! •••
  - At least on average...

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# Sequential Hash Map

• The hash table is implemented as an array of buckets, each pointing to a list of items



- Problem: If many items are added, the lists get long → Inefficient lookups!
- Solution: Resize!

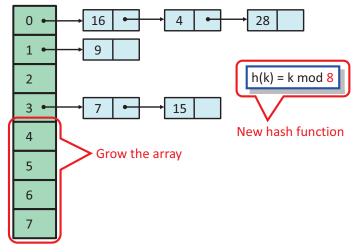
#### Hashing

- A hash function maps the items to integers
  - h: items  $\rightarrow$  integers
- Uniformly distributed
  - Different items "most likely" have different hash values
- In Java there is a hashCode() method



Resizing

• The array size is doubled and the hash function adjusted



#### Resizing

• Some items have to be moved to different buckets!

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#### Hash Sets

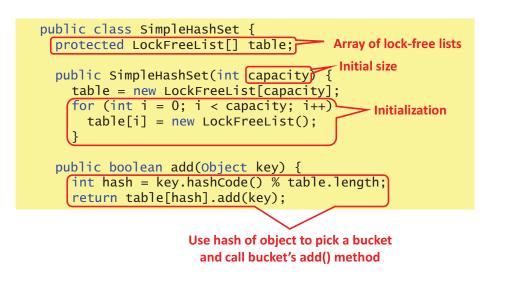
- A Hash set implements a set object
  - Collection of items, no duplicates
  - add(), remove(), contains() methods
- More coding ahead!





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#### Simple Hash Set



#### Simple Hash Set: Evaluation

- We just saw a
  - Simple
  - Lock-free
  - Concurrent

hash-based set implementation

- But we don't know how to resize...
- Is Resizing really necessary?
  - Yes, since constant-time method calls require constant-length buckets and a table size proportional to the set size
  - As the set grows, we must be able to resize



28

 $h(k) = k \mod 8$ 

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#### Set Method Mix

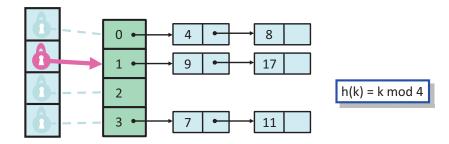
- Typical load
  - 90% contains()
  - 9% add ()
  - 1% remove()
- Growing is important, shrinking not so much
- When do we resize?
- There are many reasonable policies, e.g., pick a threshold on the number of items in a bucket
- Global threshold
  - − When, e.g.,  $\ge$  ¼ buckets exceed this value
- Bucket threshold
  - When any bucket exceeds this value



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# **Fine-Grained Locking**

• Each lock is associated with one bucket



• After acquiring the lock of the list, insert the item in the list!

# Coarse-Grained Locking

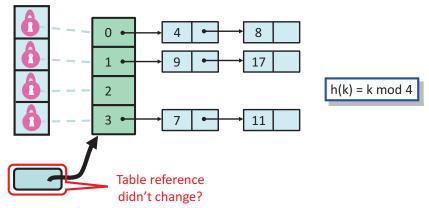
- If there are concurrent accesses, how can we safely resize the array?
- As with the linked list, a straightforward solution is to use coarse-grained locking: lock the entire array!
- This is very simple and correct
- However, we again get a sequential bottleneck...
- How about fine-grained locking?



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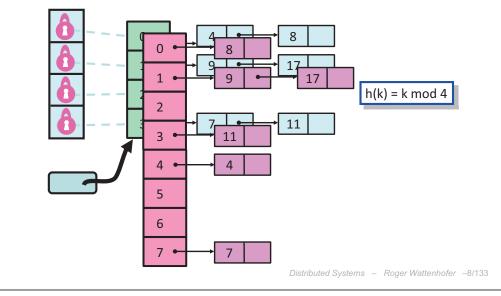
# Fine-Grained Locking: Resizing

• Acquire all locks in ascending order and make sure that the table reference didn't change between resize decision and lock acquisition!



#### Fine-Grained Locking: Resizing

• Allocate a new table and copy all elements



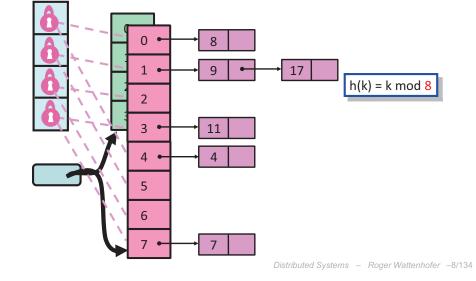
## Observations

- We grow the table, but we don't increase the number of locks
  - Resizing the lock array is tricky ...
- We use sequential lists (coarse-grained locking)
  - No lock-free list
  - If we're locking anyway, why pay?

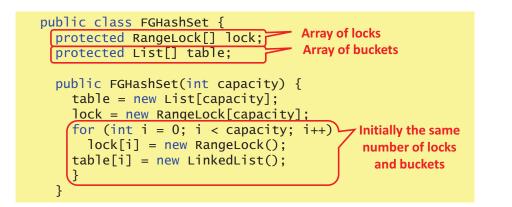


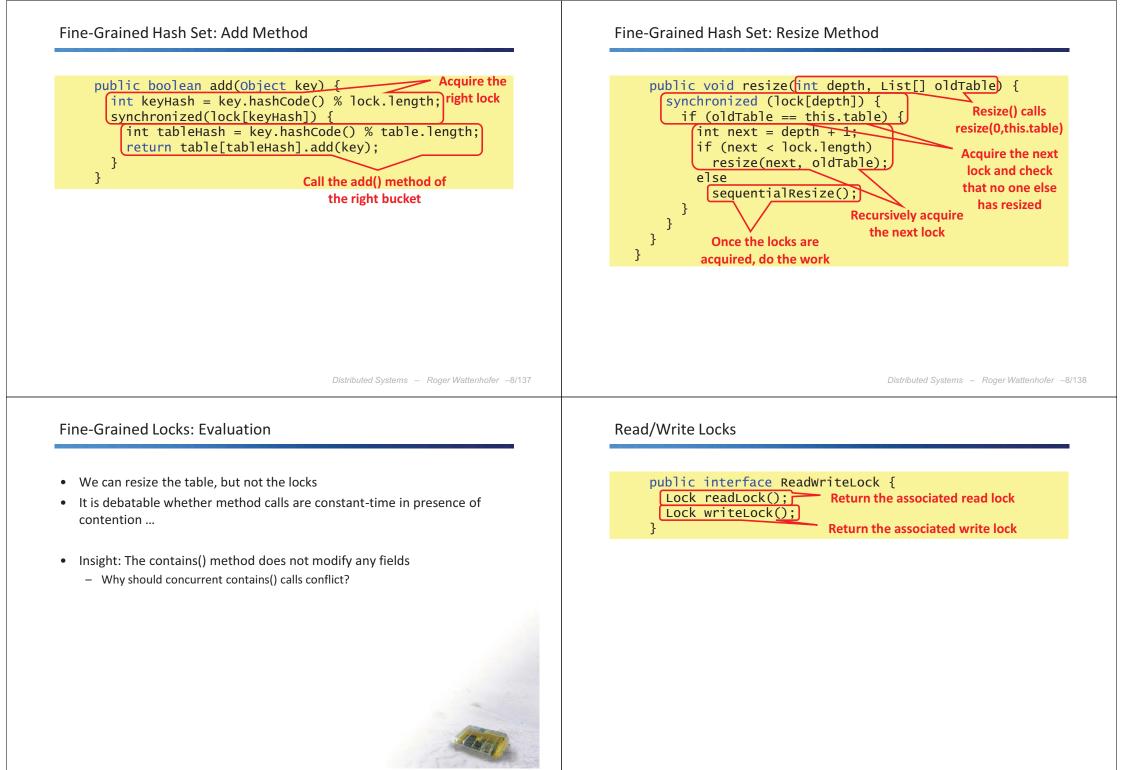
## Fine-Grained Locking: Resizing

- Stripe the locks: Each lock is now associated with two buckets
- Update the hash function and the table reference



# Fine-Grained Hash Set





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## Lock Safety Properties

- No thread may acquire the write lock
  - while any thread holds the write lock
  - or the read lock
- No thread may acquire the read lock
  - while any thread holds the write lock
- Concurrent read locks OK
- This satisfies the following safety properties
  - If readers > 0 then writer == false
  - If writer = true then readers == 0



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# **Optimistic Synchronization**

- What if the contains() method scans without locking ...?
- If it finds the key

We won't discuss this in this lecture

- It is ok to return true!
- Actually requires a proof...
- What if it doesn't find the key?
  - It may be a victim of resizing...
  - Get a read lock and try again!
  - This makes sense if is expected (?) that the key is there and resizes are rare...



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## Read/Write Lock: Liveness

- How do we guarantee liveness?
  - If there are lots of readers, the writers may be locked out!
- Solution: FIFO Read/Write lock
  - As soon as a writer requests a lock, no more readers are accepted
  - Current readers "drain" from lock and the writers acquire it eventually



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# Stop The World Resizing

- The resizing we have seen up till now stops all concurrent operations
- Can we design a resize operation that will be incremental?
- We need to avoid locking the table...

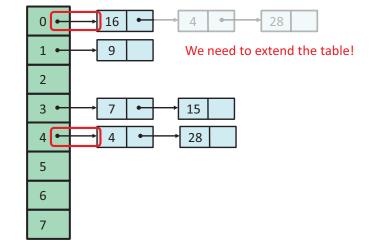
How...?

• We want a lock-free table with incremental resizing!



#### Lock-Free Resizing Problem

• In order to remove and then add even a single item, "single location CAS' is not enough...

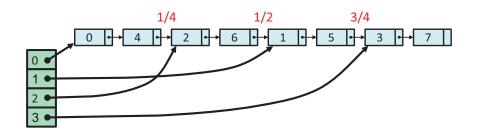


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How...?

# Recursive Split Ordering

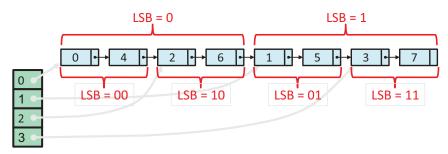
- Example: The items 0 to 7 need to be hashed into the table
- Recursively split the list the buckets in half:



• The list entries are sorted in an order that allows recursive splitting

# **Recursive Split Ordering**

• Note that the least significant bit (LSB) is 0 in the first half and 1 in the other half! The second LSB determines the next pointers etc.



• Move the buckets instead of the items!

Idea: Don't Move the Items

16

0

1 •

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- Keep all items in a single lock-free list
- Buckets become "shortcut pointers" into the list

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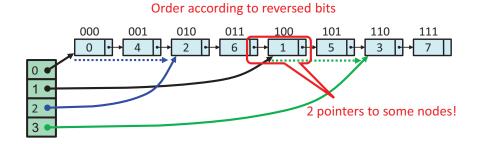
#### Split-Order

- If the table size is 2<sup>i</sup>:
  - Bucket b contains keys k = b mod 2<sup>i</sup>
  - The bucket index consists of the key's i least significant bits
- When the table splits:
  - Some keys stay (b = k mod 2<sup>i+1</sup>)
  - Some keys move (b+2<sup>i</sup> = k mod2<sup>i+1</sup>)
- If a key moves is determined by the (i+1)<sup>st</sup> bit
  - counting backwards

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#### Split Ordered Hashing

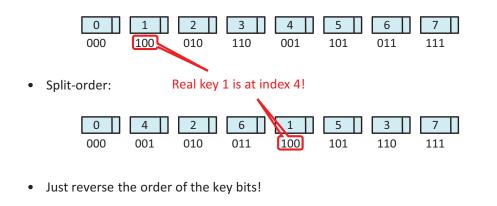
• After a resize, the new pointers are found by searching for the right index



• A problem remains: How can we remove a node by means of a CAS if two sources point to it?

# A Bit of Magic

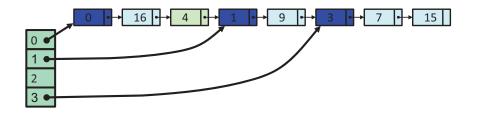
- We need to map the real keys to the split-order
- Look at the binary representation of the keys and the indices
- The real keys:



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#### Sentinel Nodes

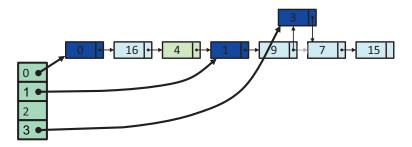
• Solution: Use a sentinel node for each bucket



- We want a sentinel key for i ordered
  - before all keys that hash to bucket i
  - after all keys that hash to bucket (i-1)

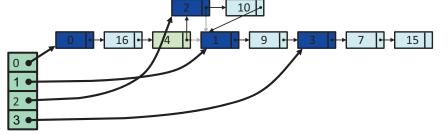
#### Initialization of Buckets

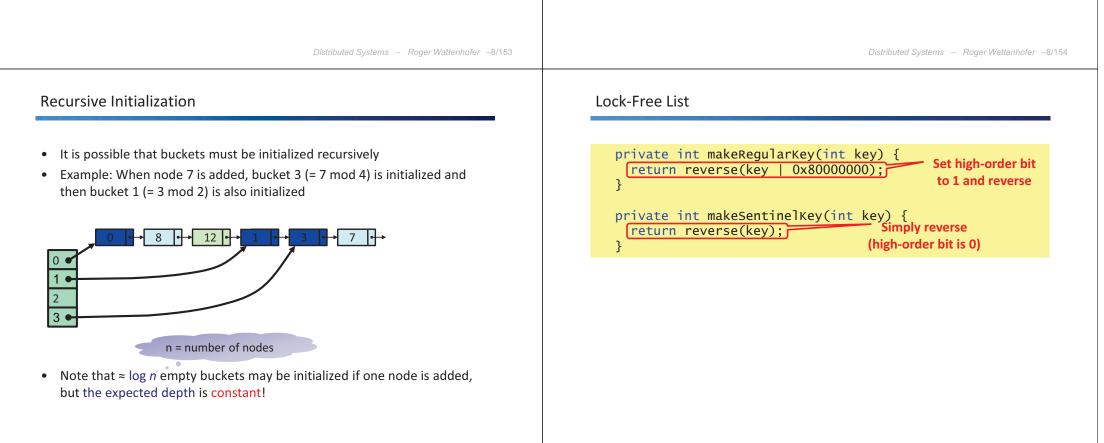
- We can now split a bucket in a lock-free manner using two CAS() calls
- Example: We need to initialize bucket 3 to split bucket 1!

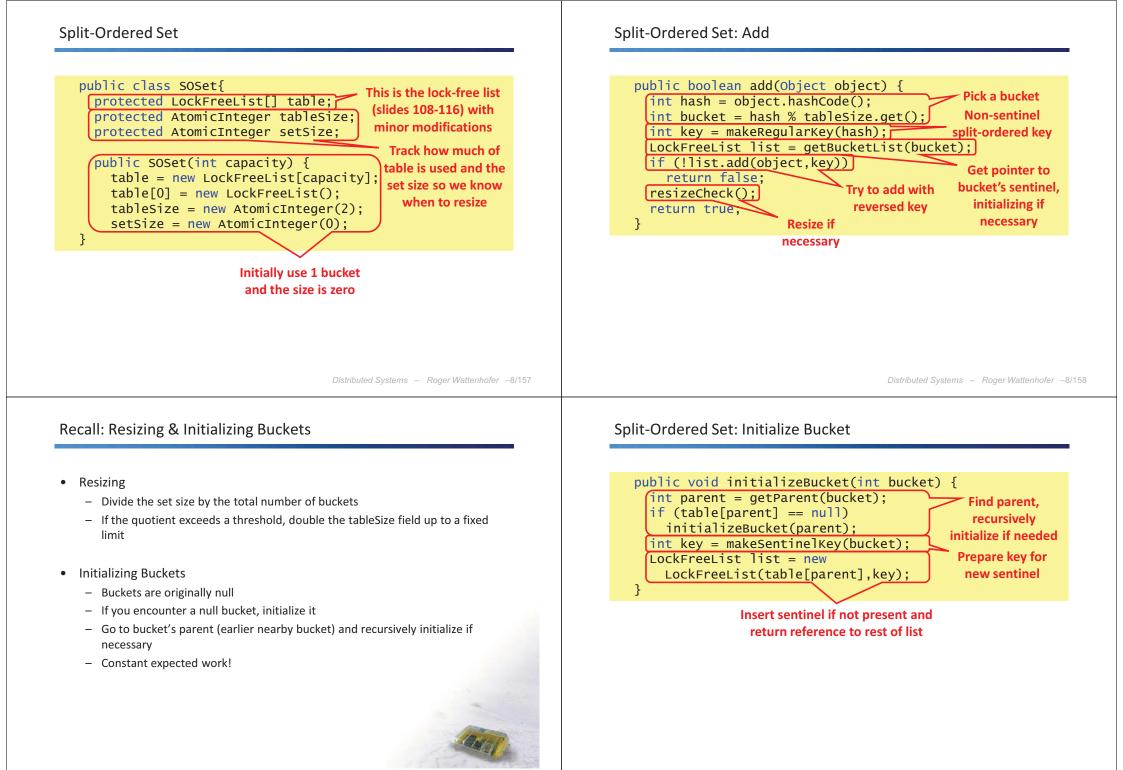


#### Adding Nodes

- Example: Node 10 is added
- First, bucket 2 (= 10 mod 4) must be initialized, then the new node is added







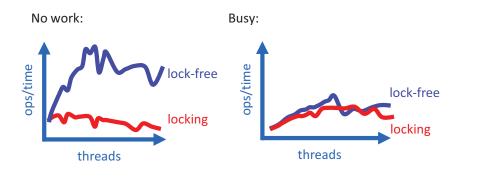
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#### Correctness

- Split-ordered set is a correct, linearizable, concurrent set implementation
- Constant-time operations!
  - It takes no more than O(1) items between two dummy nodes on average
  - Lazy initialization causes at most O(1) expected recursion depth in initializeBucket()

#### **Empirical Evaluation**

- Evaluation has been performed on a 30-processor Sun Enterprise 3000
- Lock-Free vs. fine-grained (Lea) optimistic locking
- In a non-multiprogrammed environment
- 10<sup>6</sup> operations: 88% contains(), 10% add(), 2% remove()

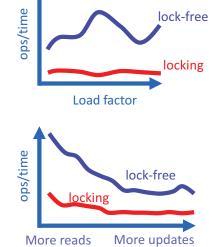


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#### **Empirical Evaluation**

- Expected bucket length
  - The load factor is the capacity of the individual buckets



- Varying The Mix
  - Increasing the number of updates